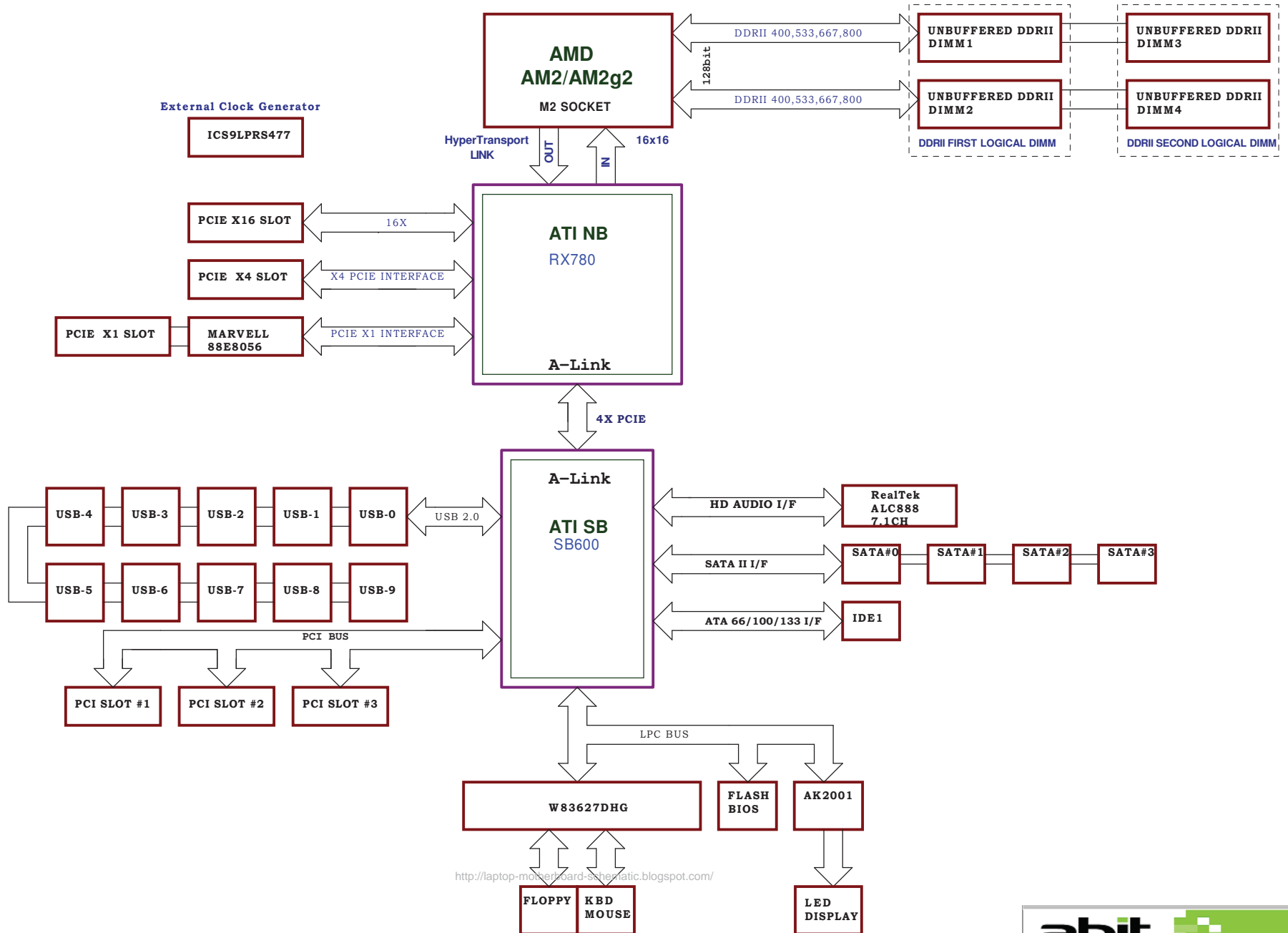


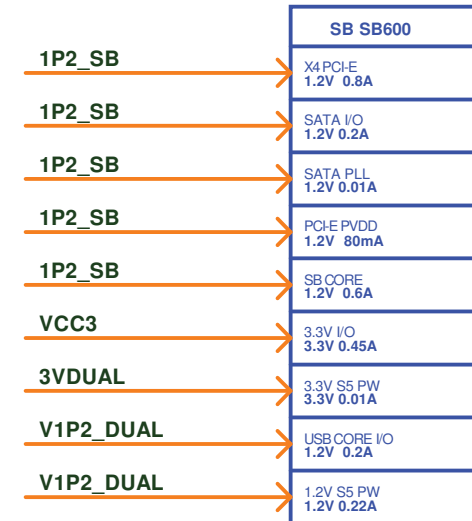
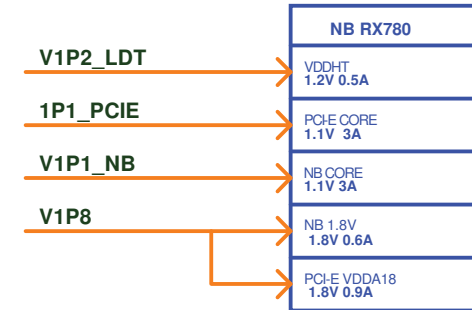
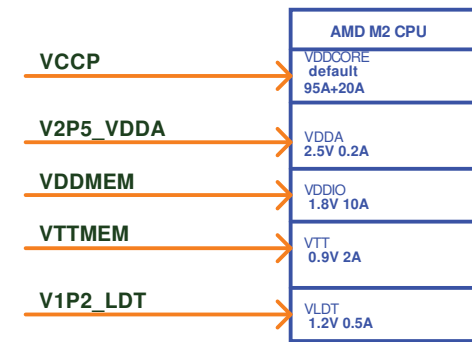
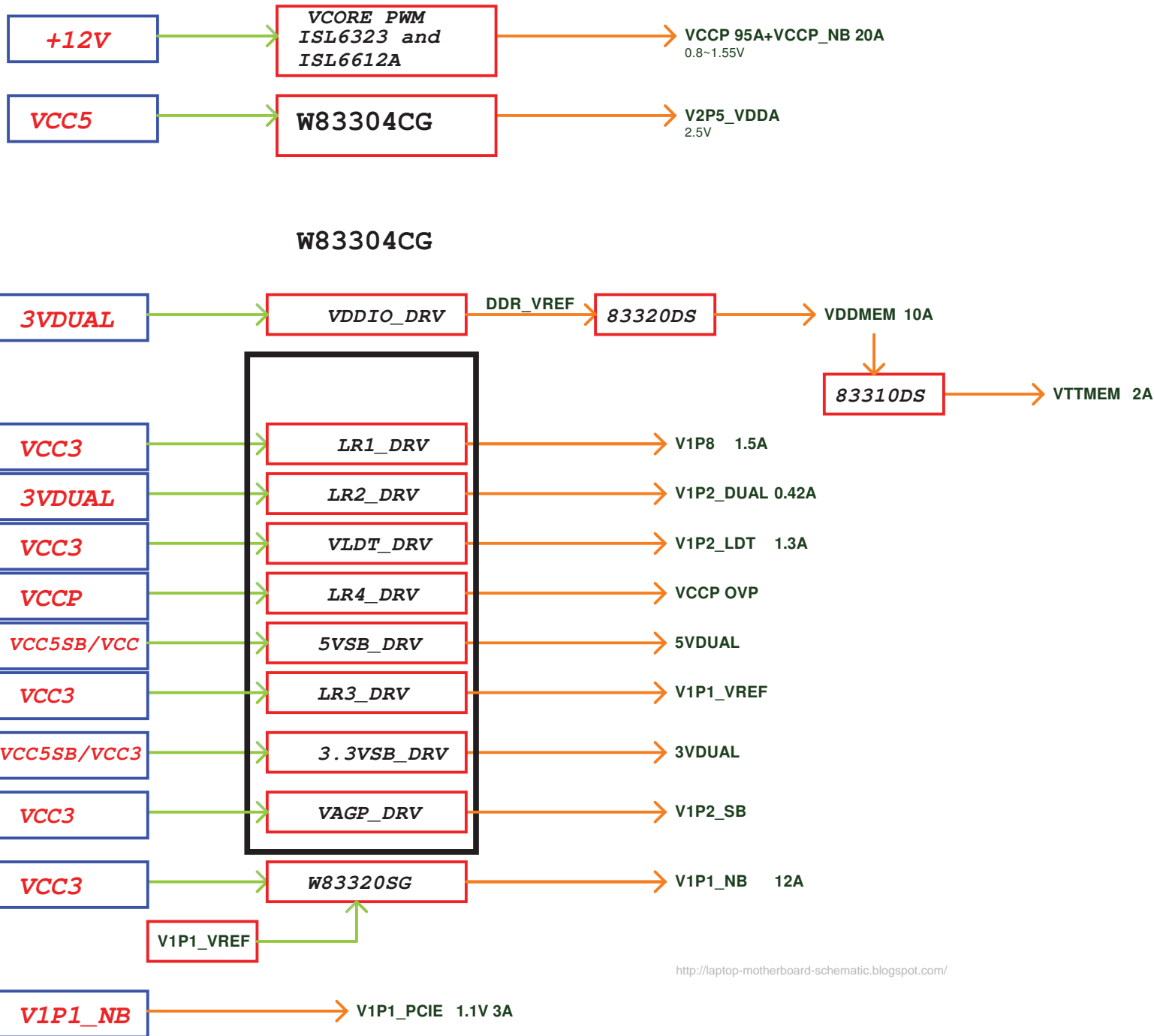
abit AX78 V0.2 SCHEMATICS

TITLE	SHEET
SYSTEM BLOCK DIAGRAM	2
POWER DISTRIBUTION	3
K8 CPU	4~8
DDR2 DIMM SOCKET	9~12
AMD RX780	13~15
CLOCK GEN.	16
PCI-E X16	17
PCI-E x4/x1	18
AMD SB600	19~22
PCI SLOT 1,2,3	23
SATAII 1-4 and IDE & AK2001	24
LPC I/O W83627DHG	25
FPIO , FWH , SMB ,FDC , ATX	26
KB , MS & FP-USB 1,2,3	27
HD-AUDIO ALC888	28
PCIE GbE LAN	29
FAN & HW MONITOR	30
PWM ISL6323 & ISL6613	31~32
W83304CG	33~34
MECHANICAL	35

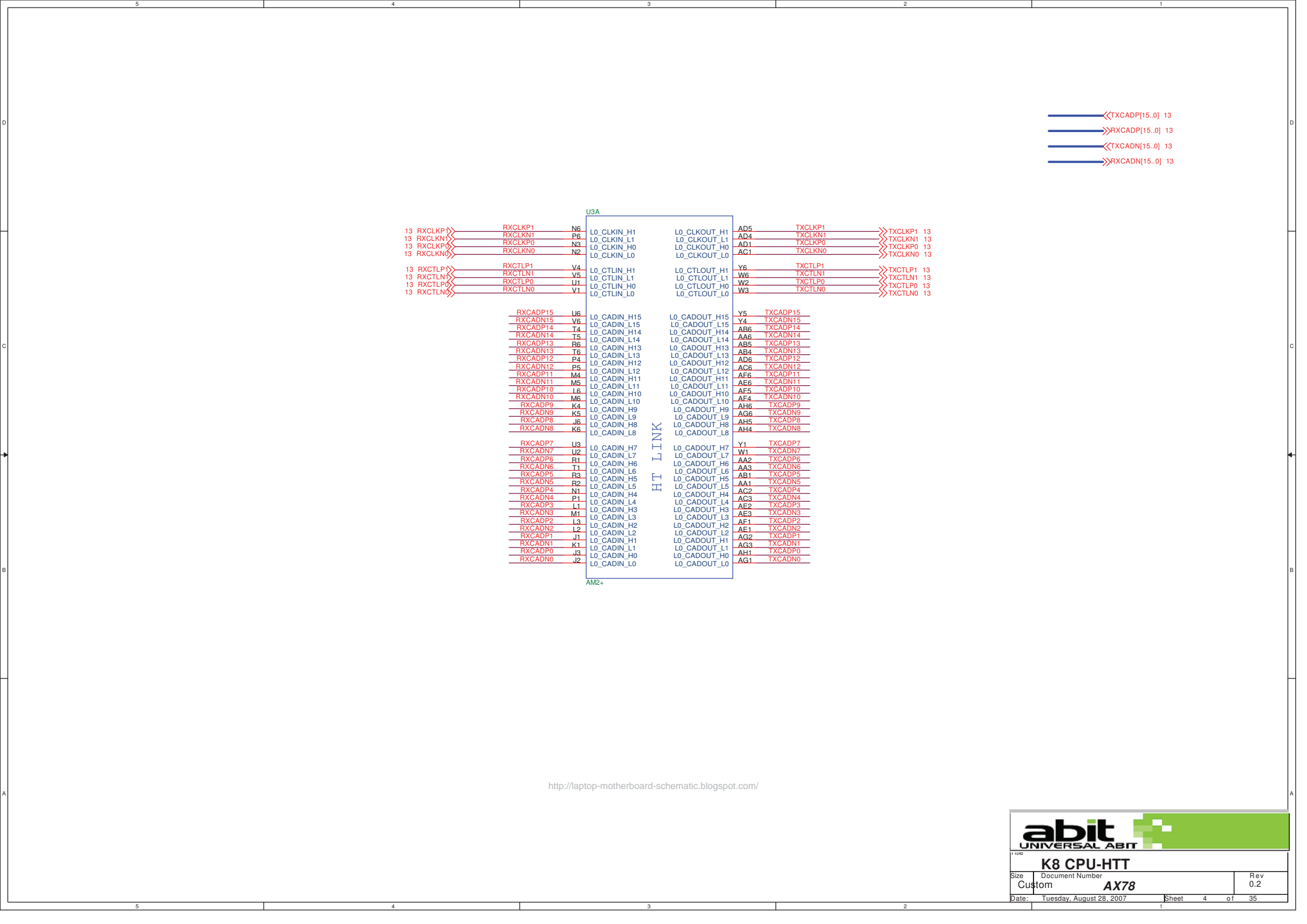
<http://laptop-motherboard-schematic.blogspot.com/>

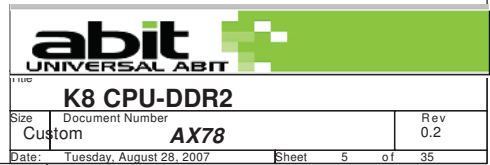
U-ABIT AX78 BLOCK DIAGRAM

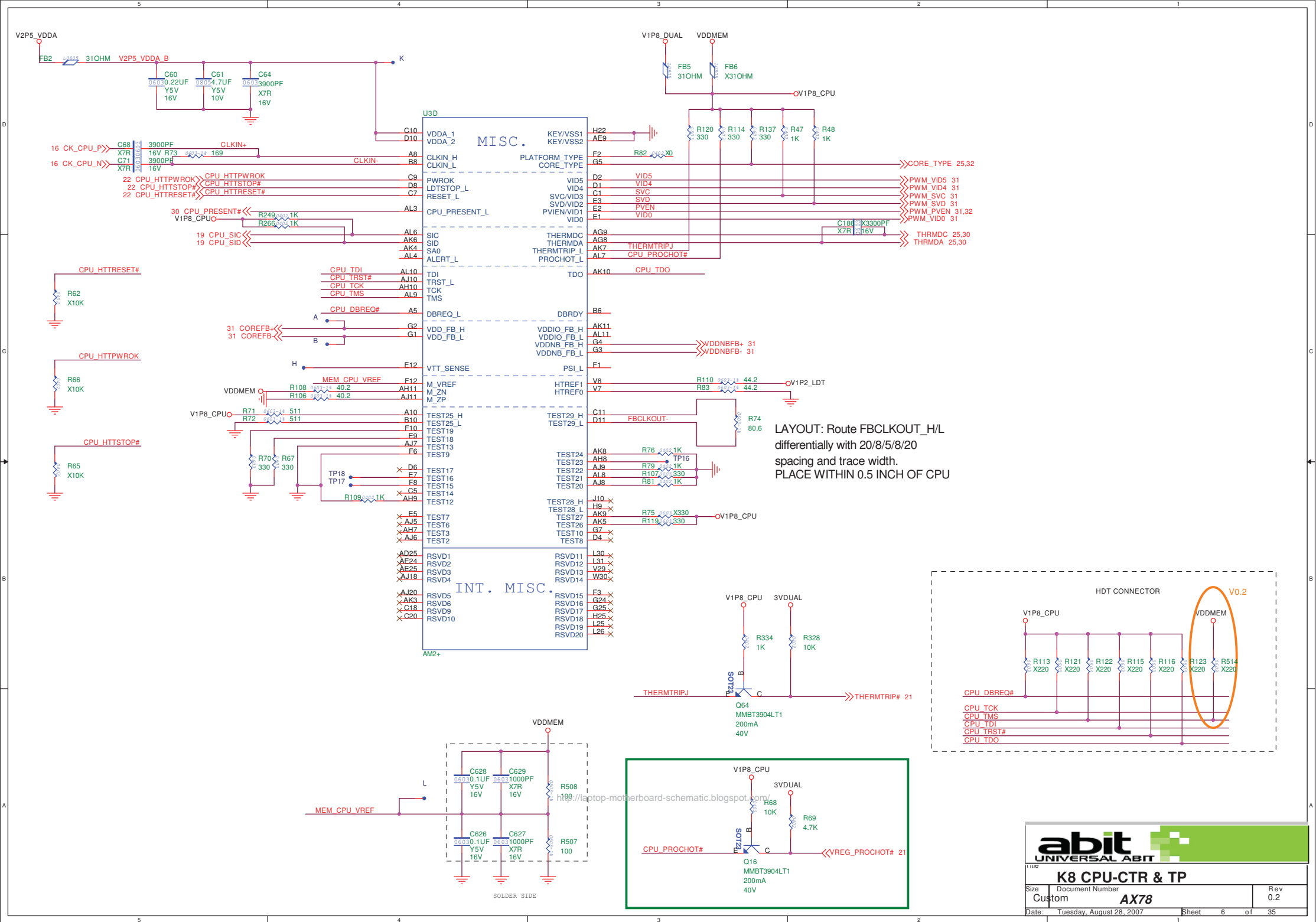




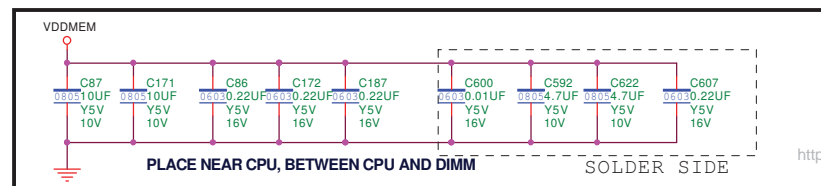
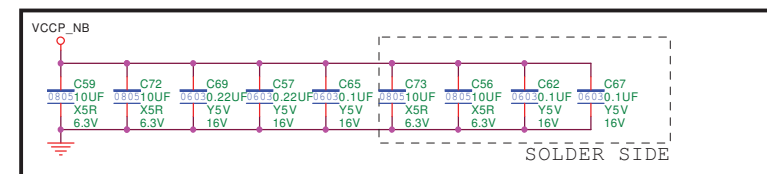
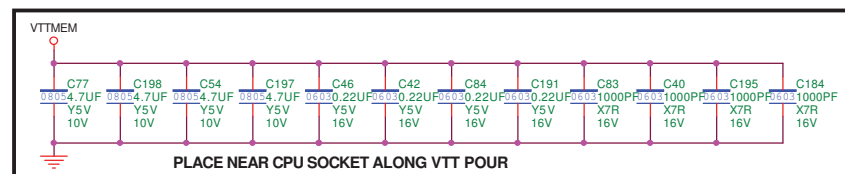
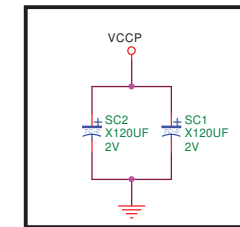
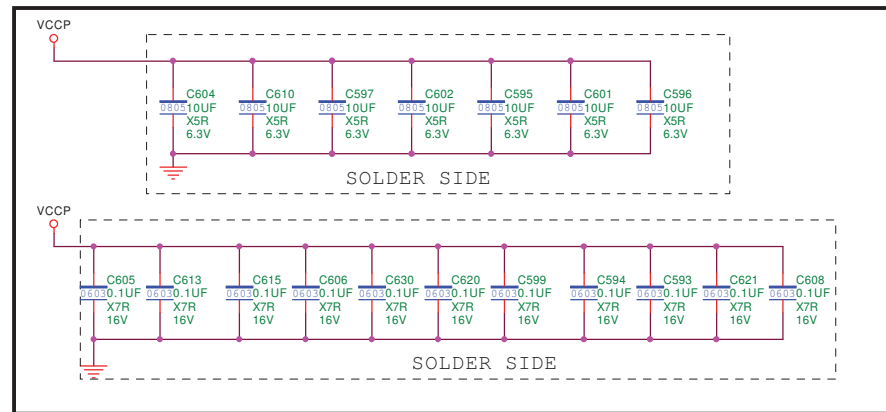
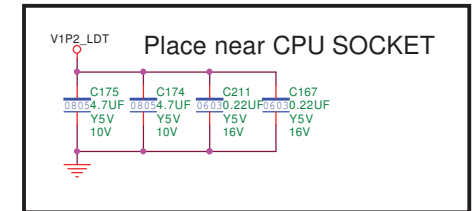
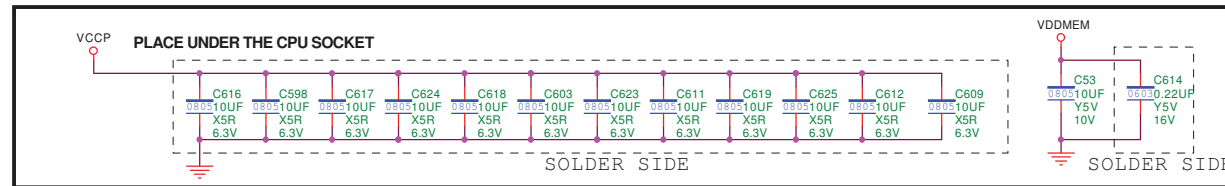
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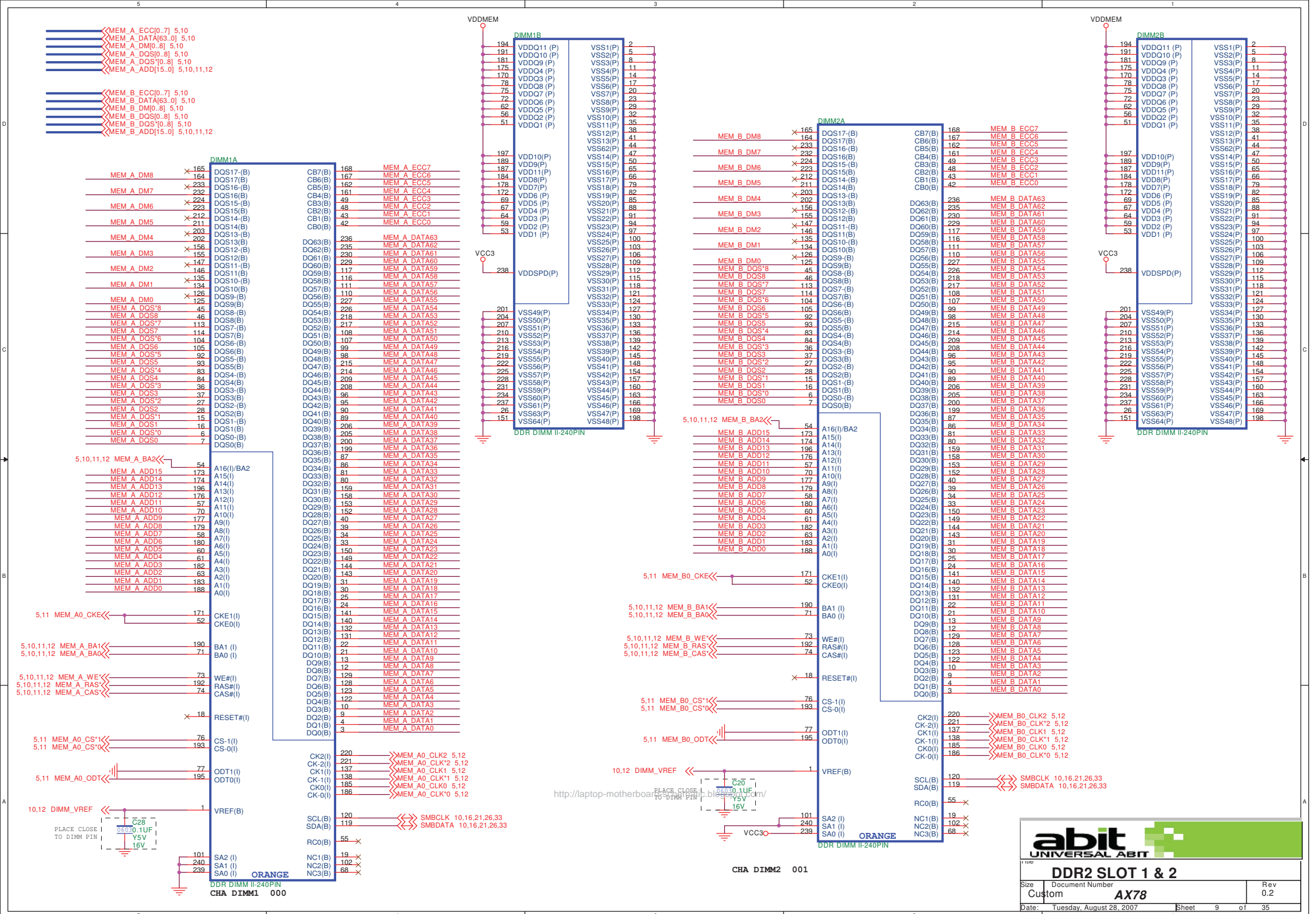


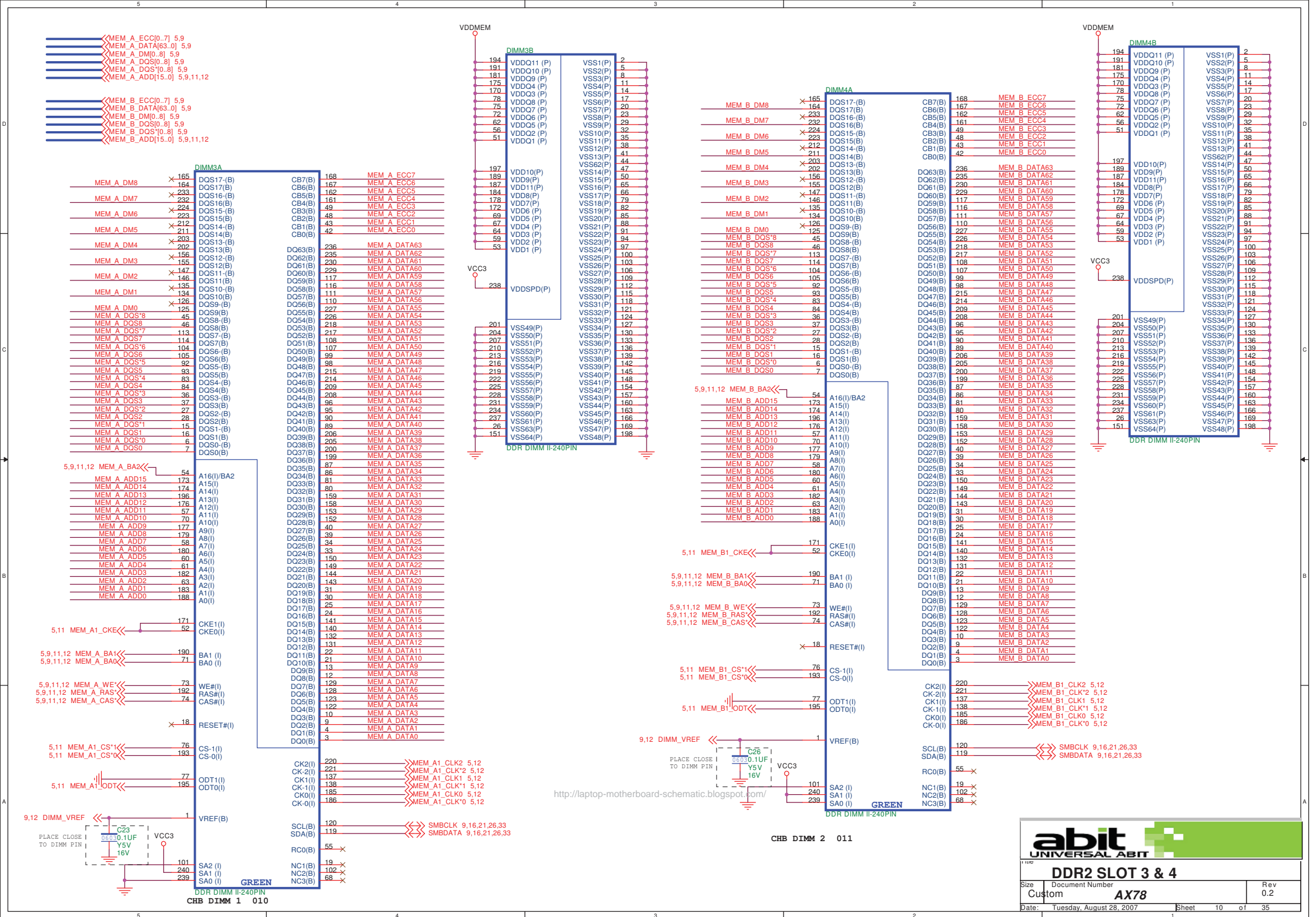


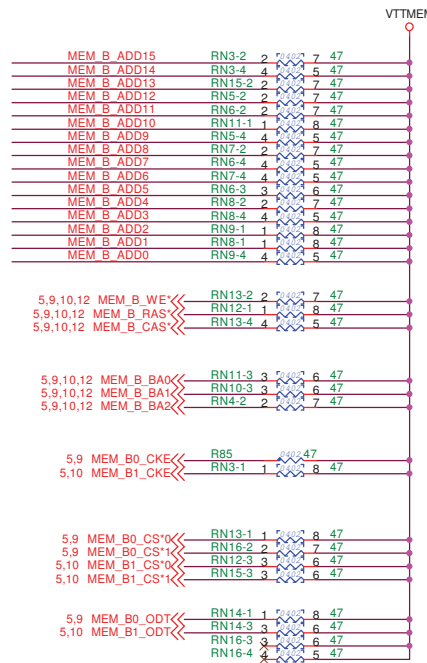
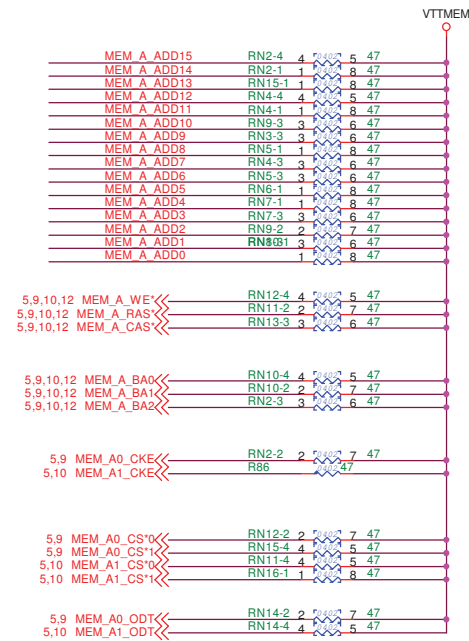
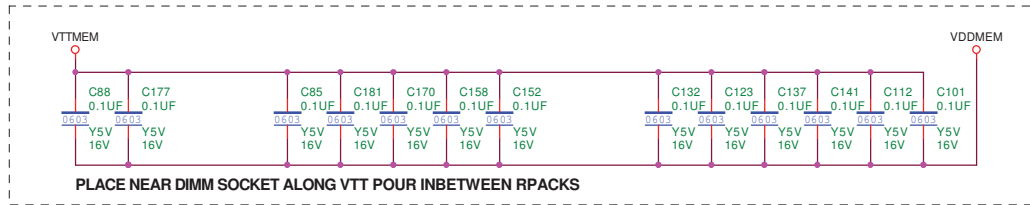
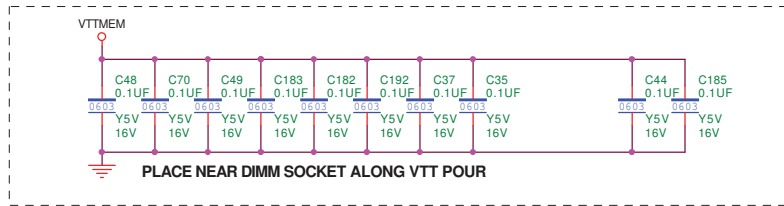
Bottomside Decoupling



<http://laptop-motherboard-schematic.blogspot.com/>



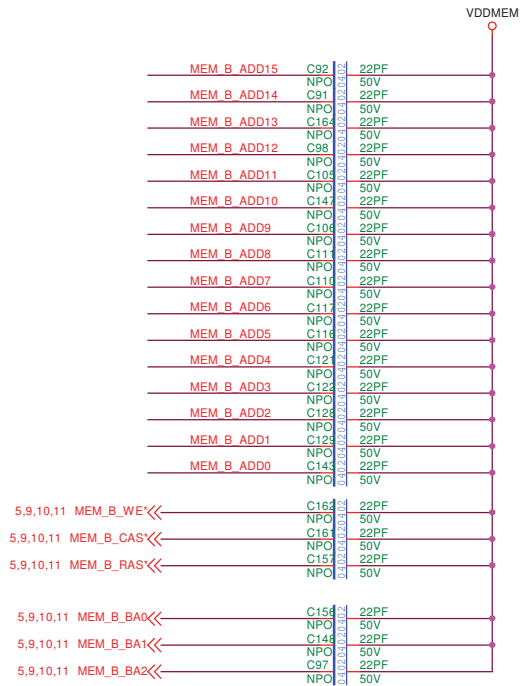
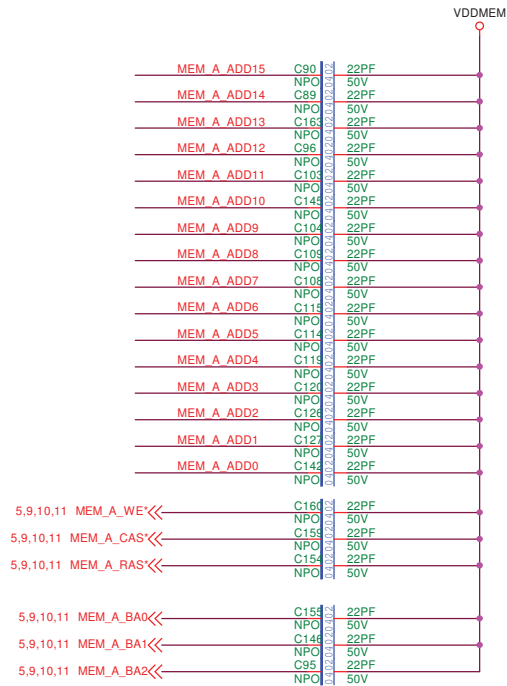




MEM_A_ADD[15..0] 5,9,10,12

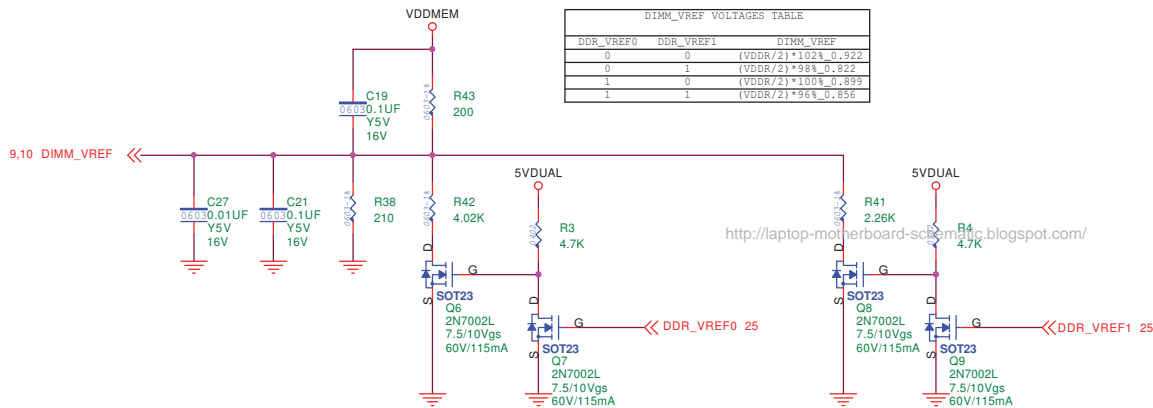
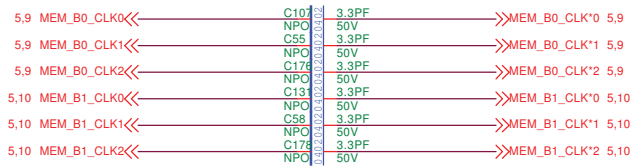
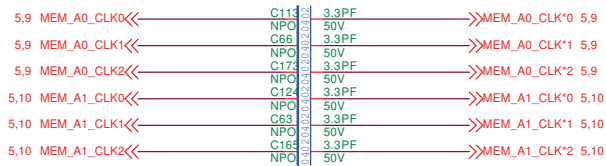
MEM_B_ADD[15..0] 5,9,10,12

<http://laptop-motherboard-schematic.blogspot.com/>

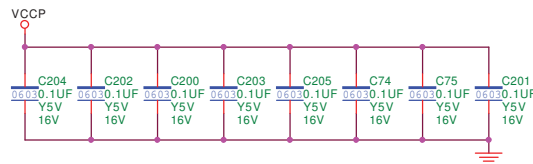
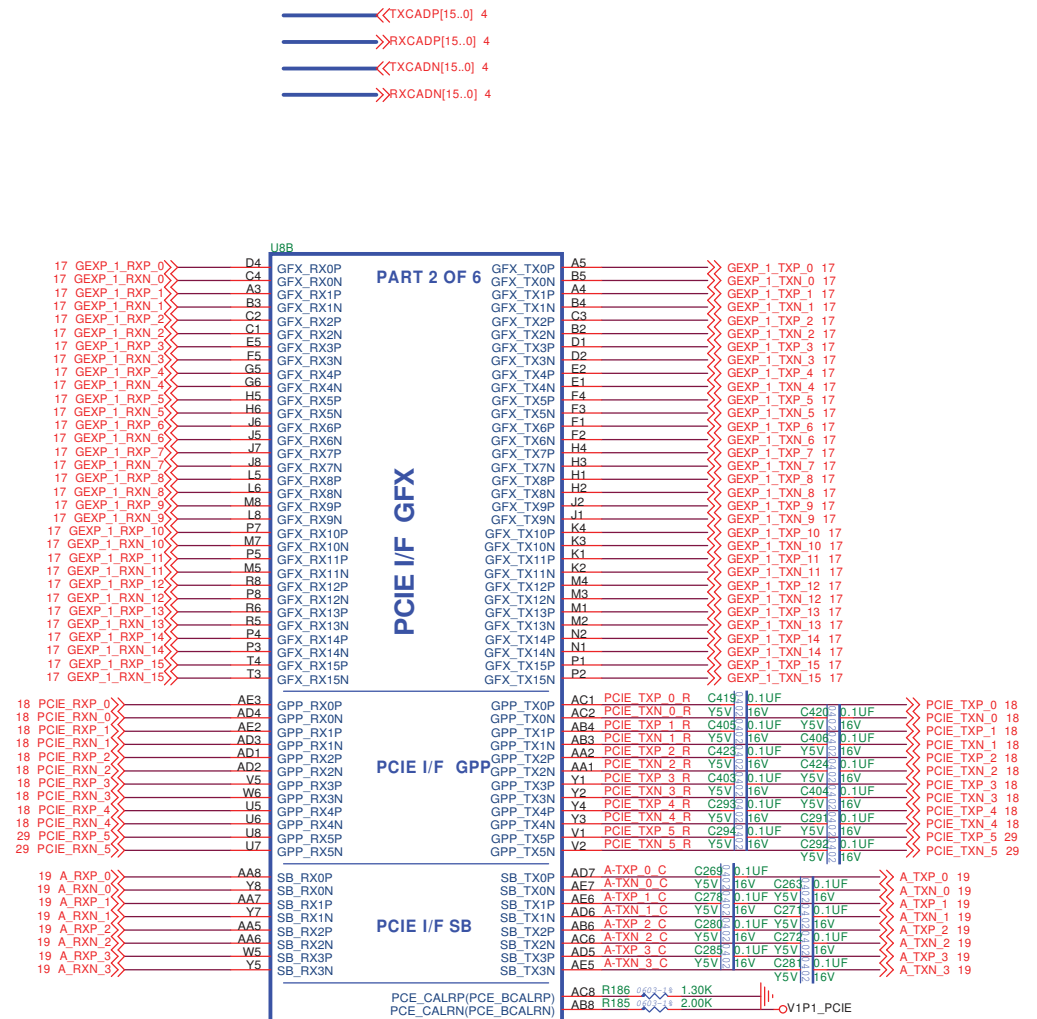
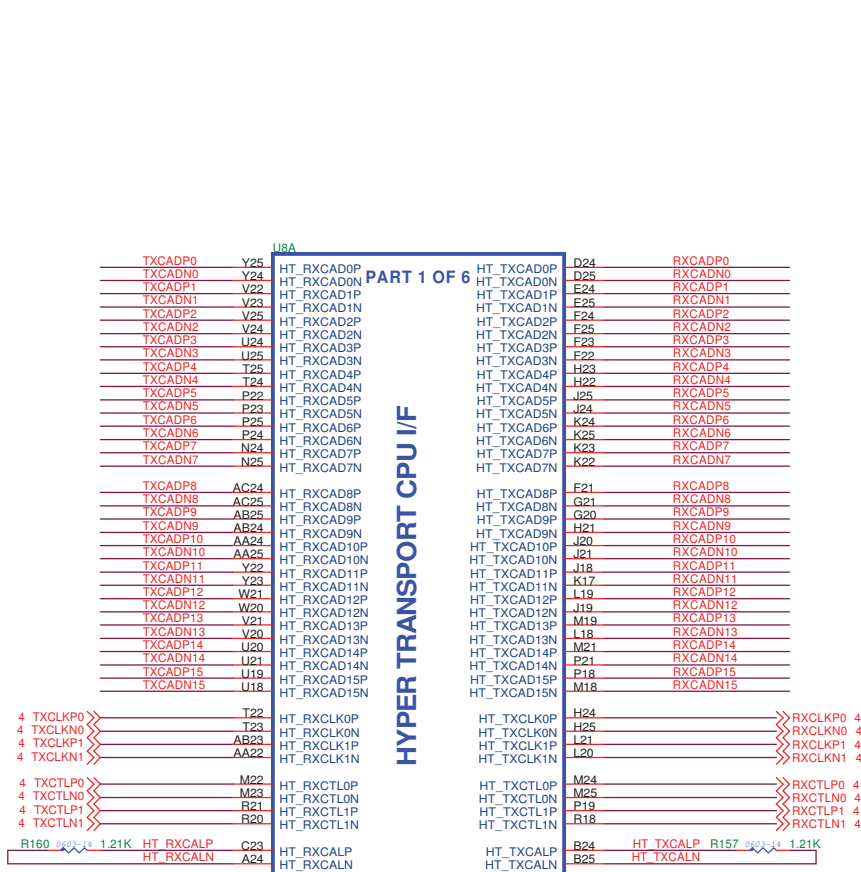


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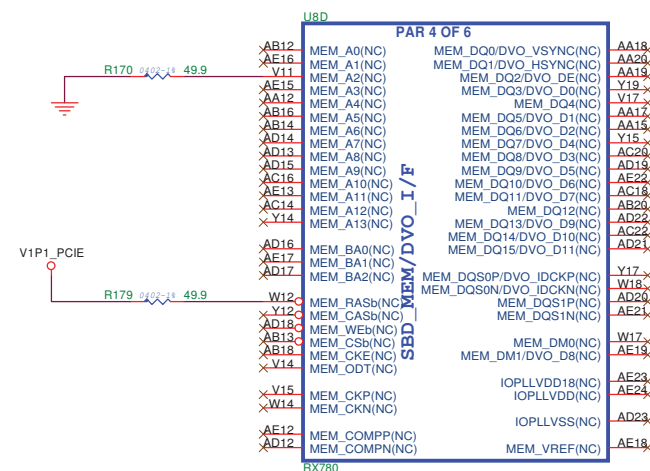
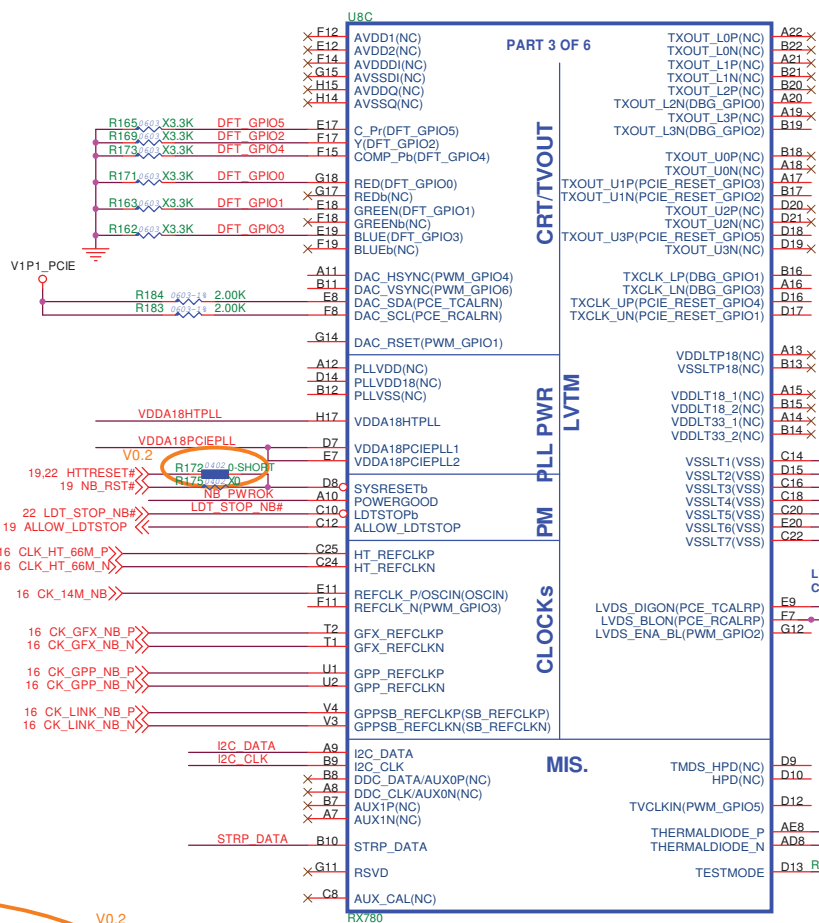
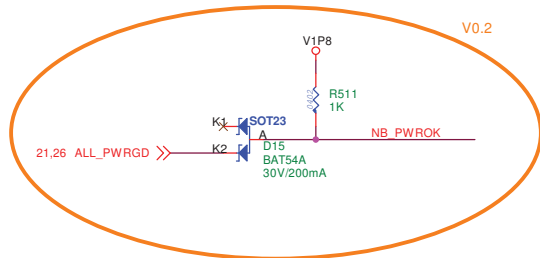
<<MEM_B_ADD[15..0] 5,9,10,11



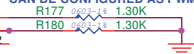
DIMM_VREF VOLTAGES TABLE		
DDR_VREF0	DDR_VREF1	DIMM_VREF
0	0	(VDDR/2)*102%_0.922
0	1	(VDDR/2)*98%_0.822
1	0	(VDDR/2)*100%_0.899
1	1	(VDDR/2)*96%_0.856



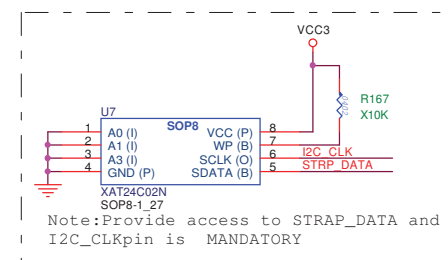
<http://laptop-motherboard-schematic.blogspot.com/>

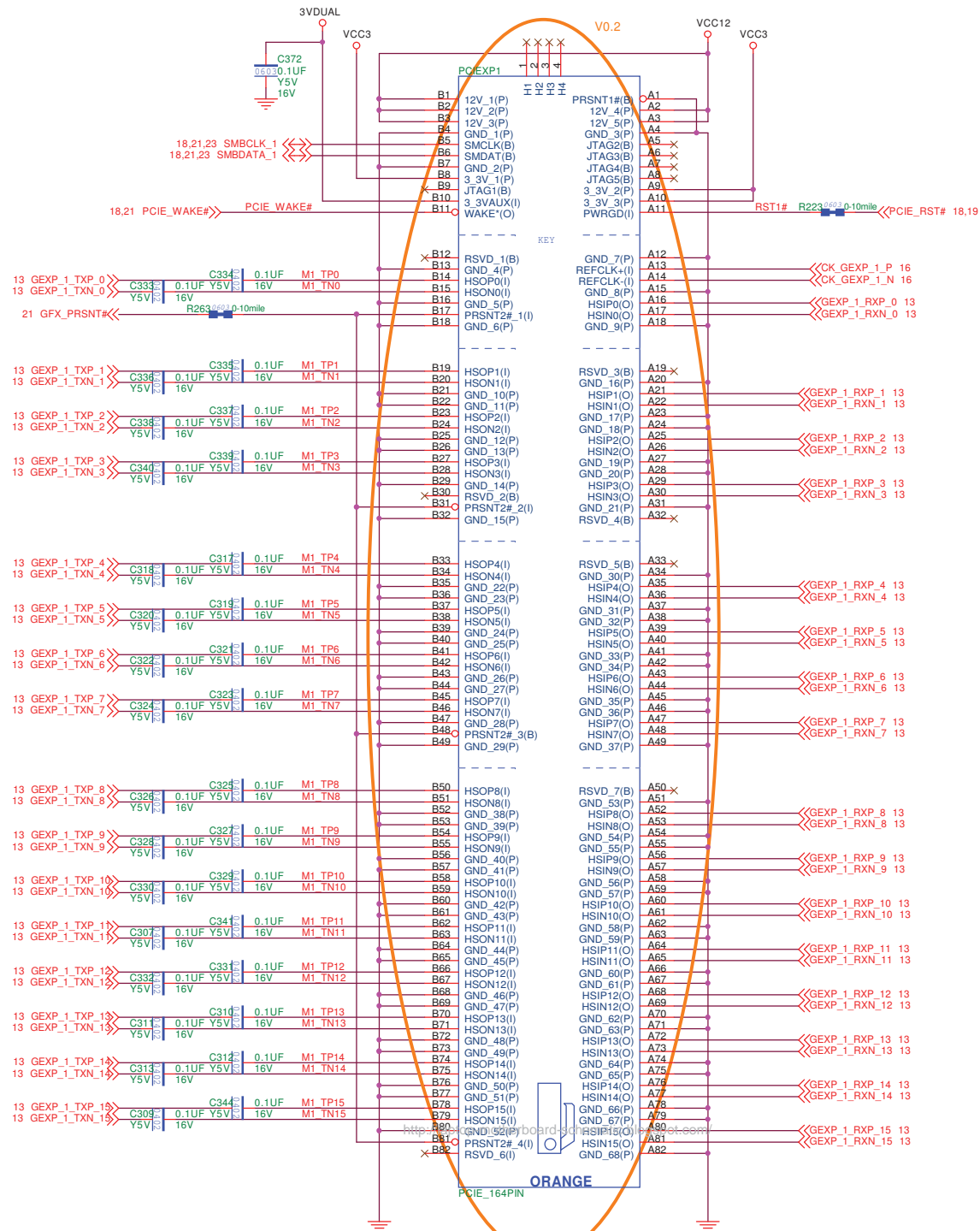


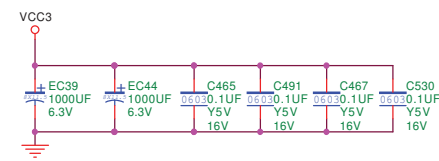
LVDS_DIGON, LVDS_BLON, LVDS_BLEN, STRP_DATA AND TMDS_HPD
CAN BE CONFIGURED AS PWM OUTPUT





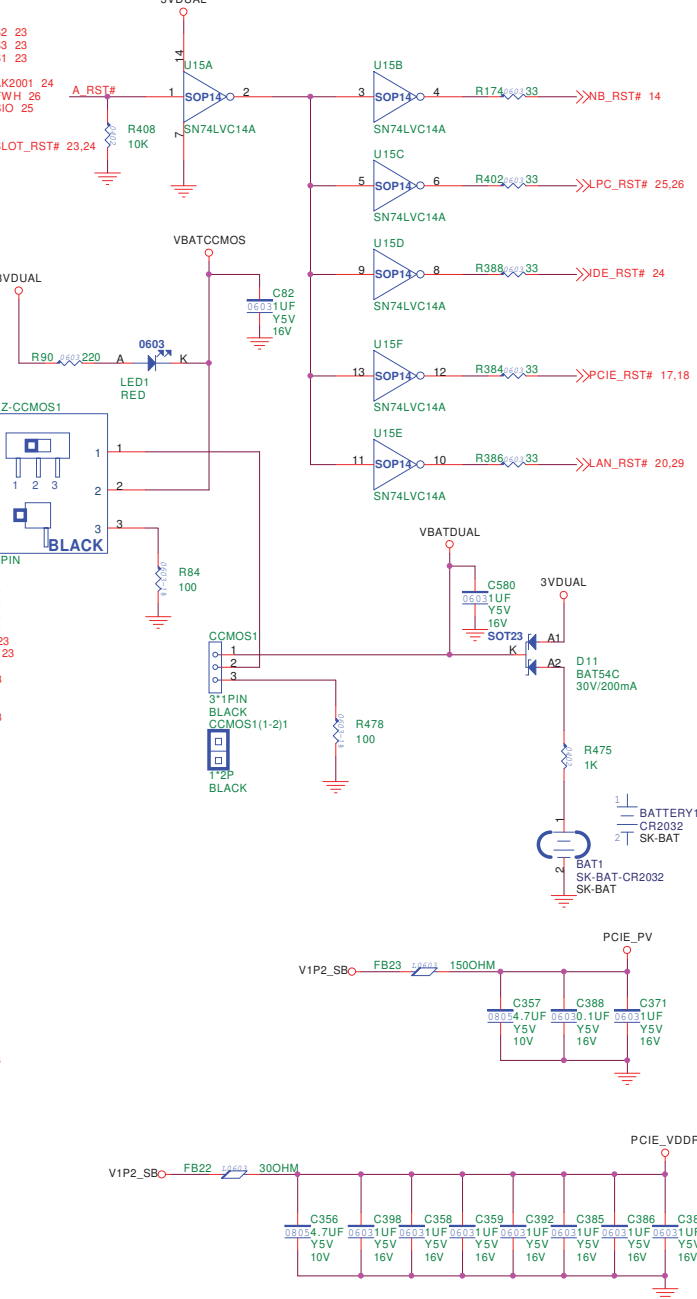
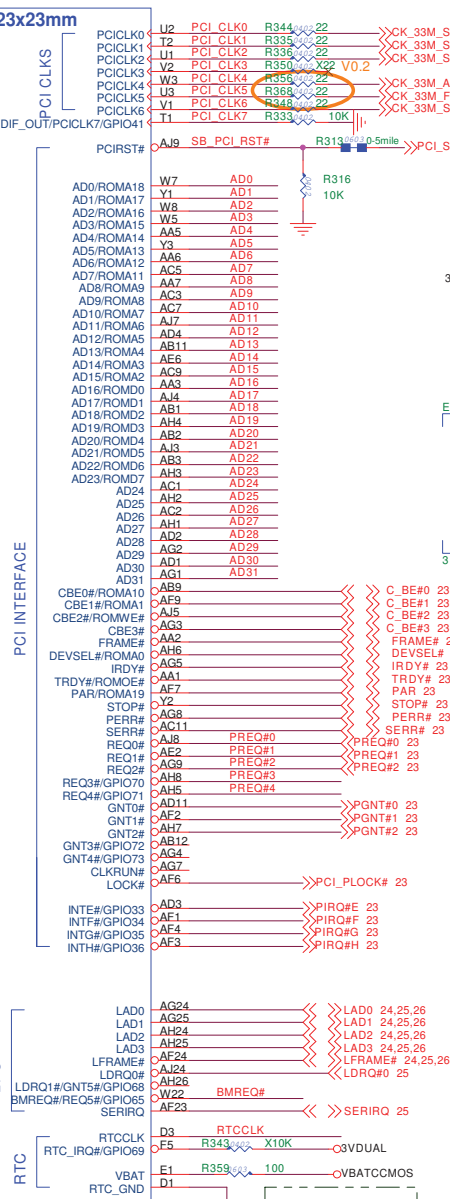
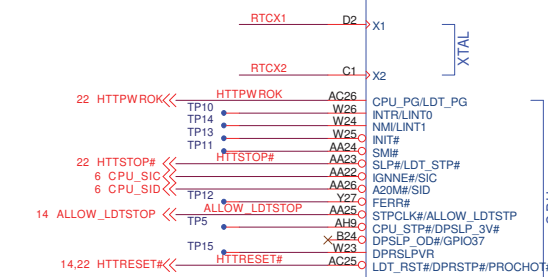
	RX780	RS690 only (NC for RS485)		
	DFT_GPIO1	DFT_GPIO0	DFT_GPIO[4:2]	DFT_GPIO5
PULL HIGH (internally pulled high)	Bypass the loading of EEPROM straps and use Hardware default values DEFAULT	Memory side port not available DEFAULT	These pin straps are used to configure PCI-E GPP mode: 111: register defined (register default to Config E) DEFAULT 110: 4-0-0-0-0 Config A 101: 4-4 Config B 100: 4-2-2 Config C 011: 4-2-1-1 Config D 010: 4-1-1-1-1 Config E others: register defined (register default to Config E)	Enable debug bus via the memory IO pads, if available in the package use default values DEFAULT
PULL LOW	I2C Master can load strap values from EEPROM if connected, or use default values if not connected	Memory side port available		use the memory data bus to output the debug bus

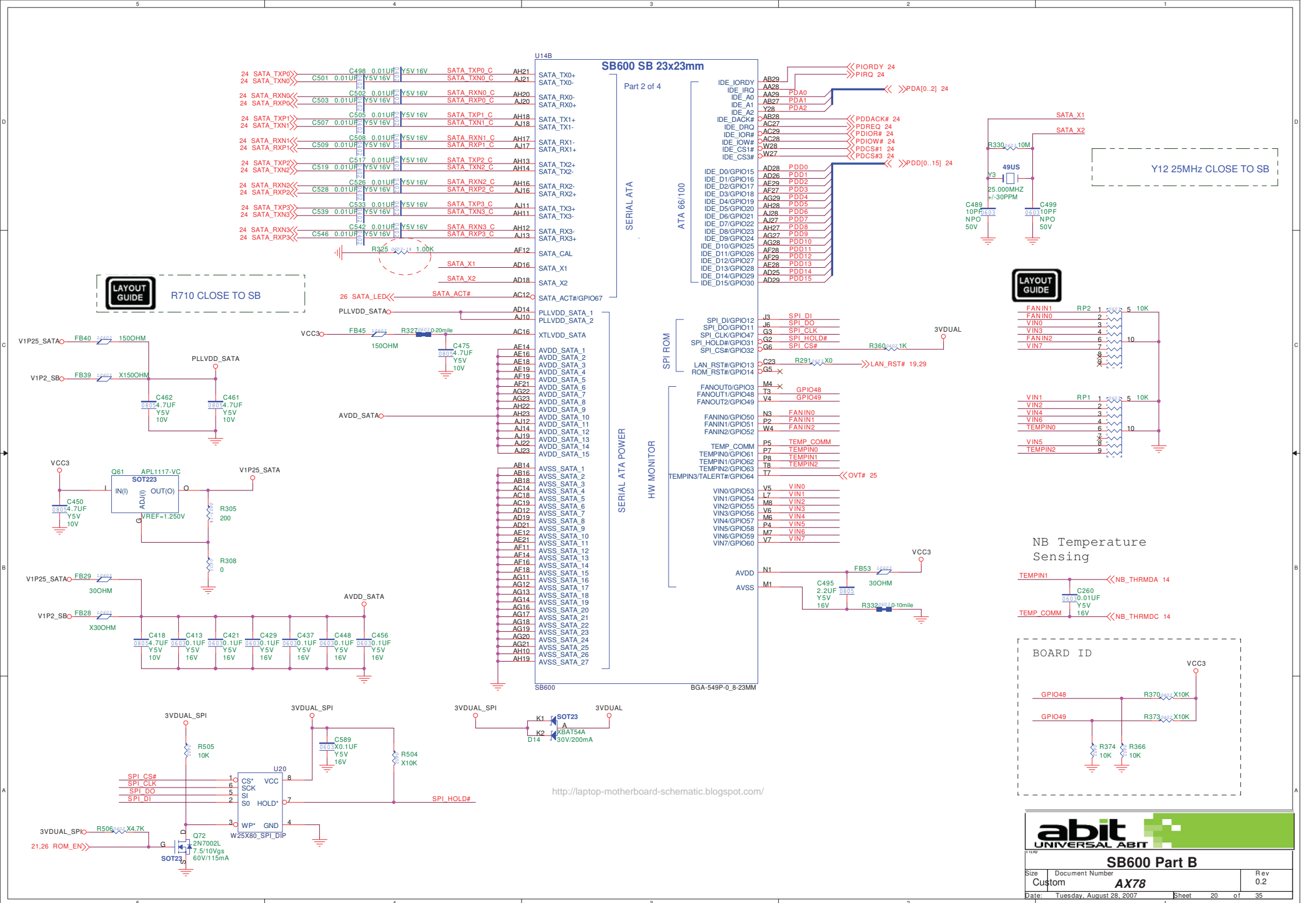


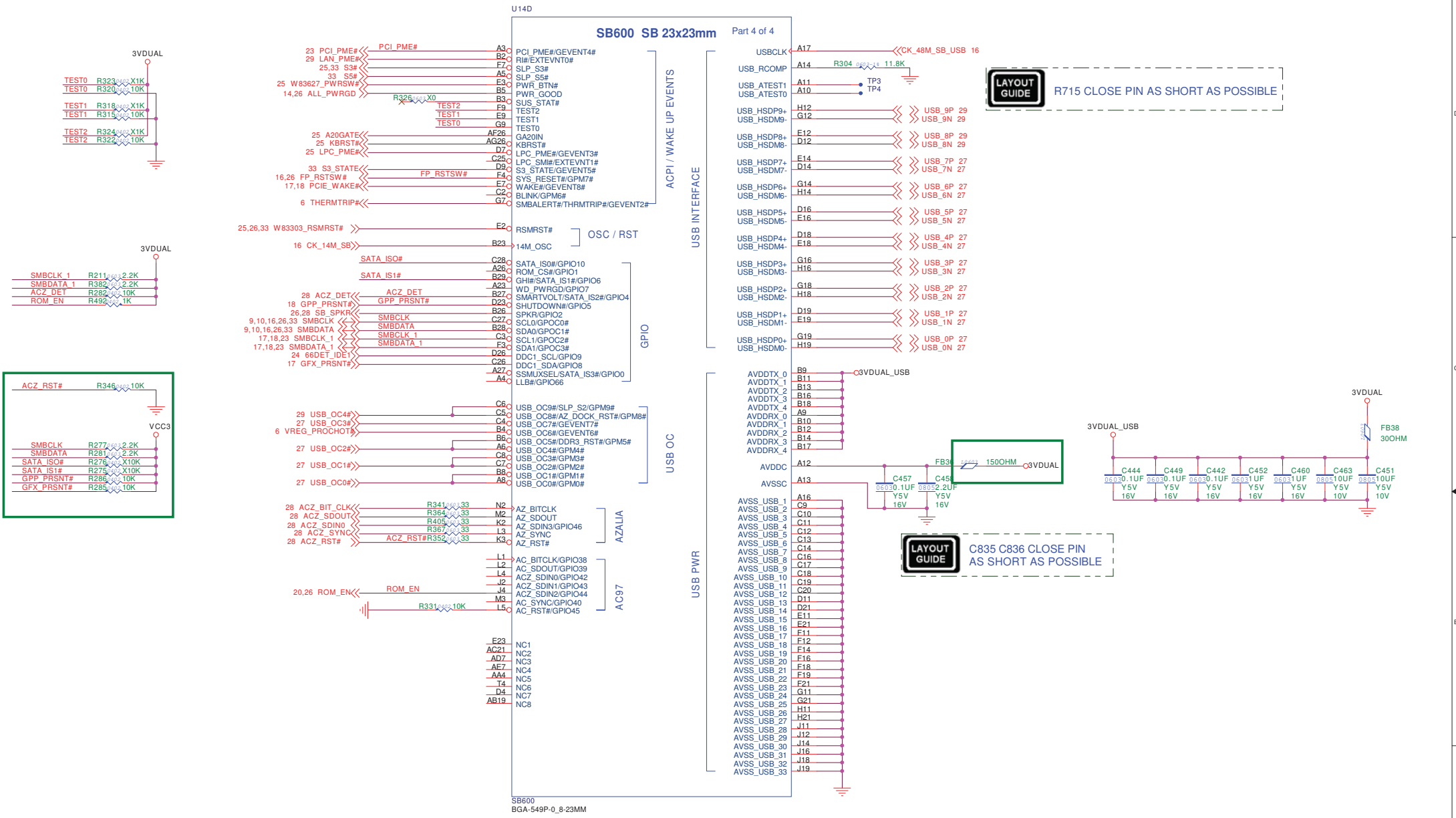




			
111000 <div style="float: right;">  </div>			
PCI-E x4/x1			
Size	Document Number	Rev	
Custom	AX78	0.2	
Date:	Tuesday, August 28, 2007	Sheet	18 of 35

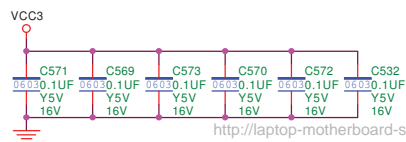
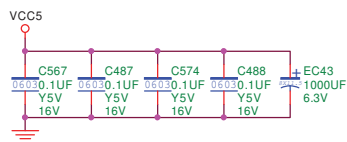
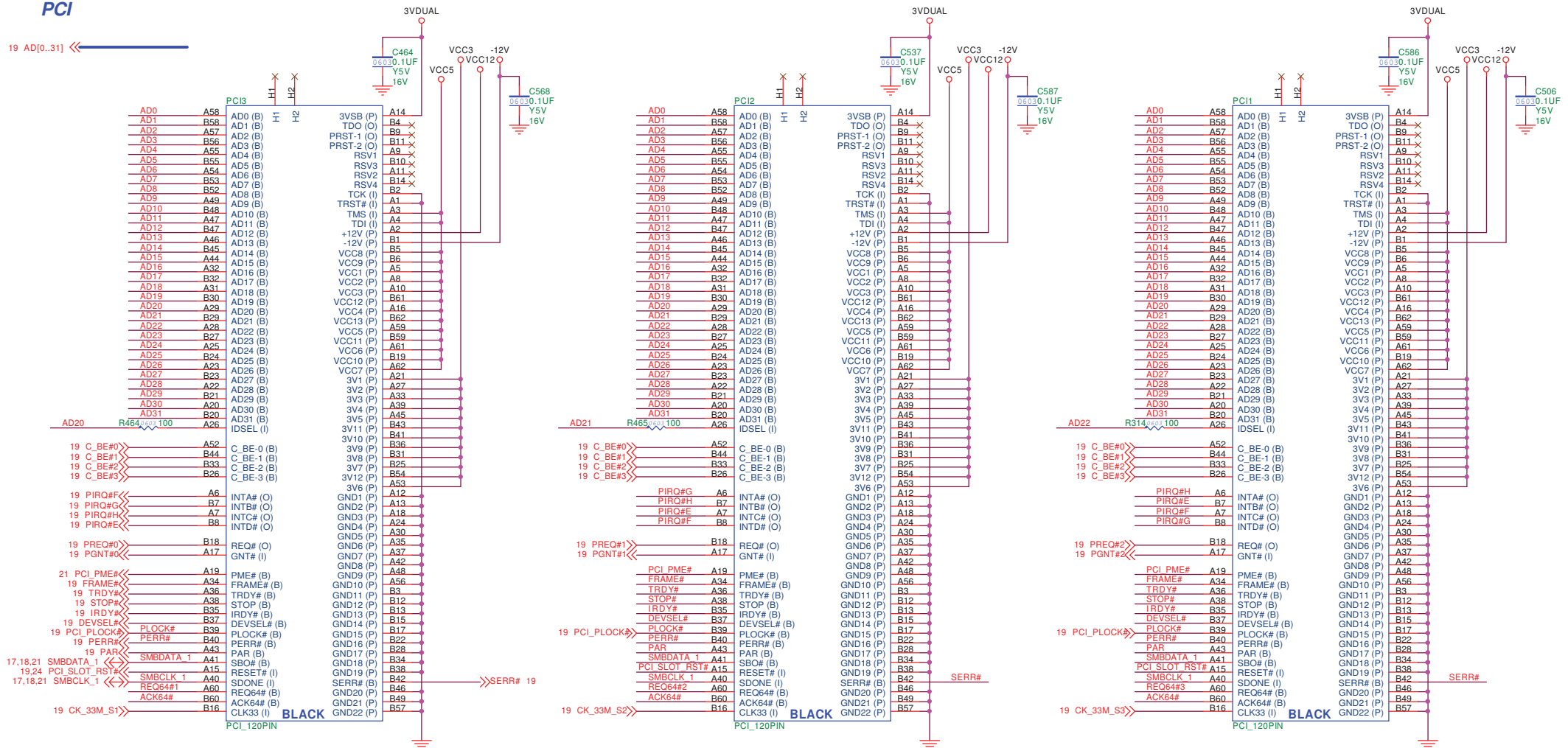


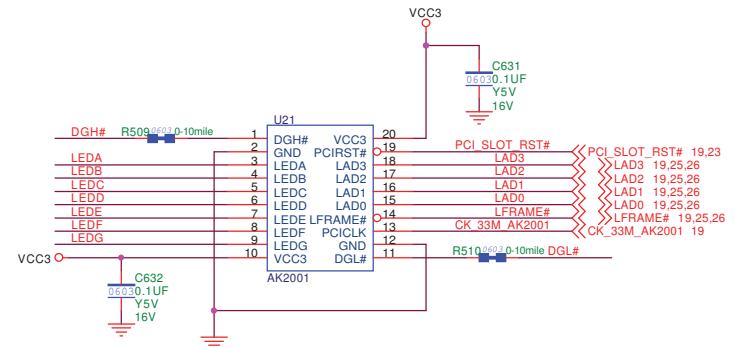
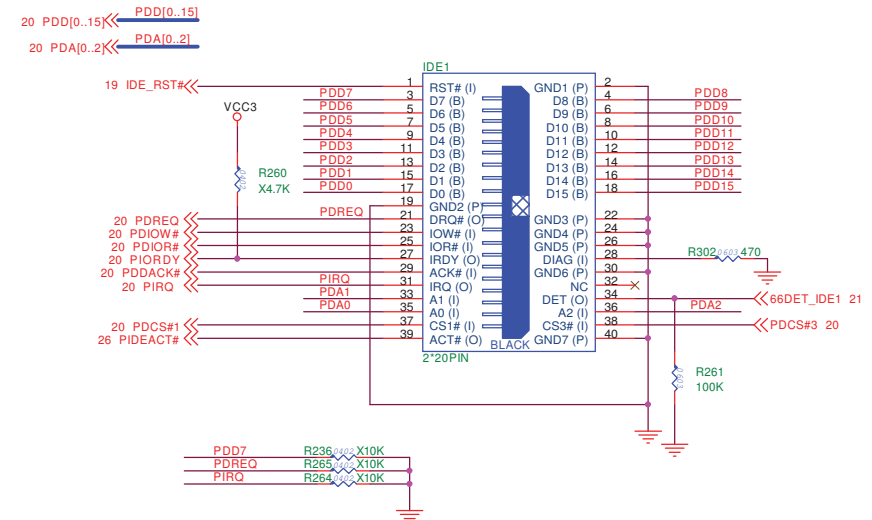
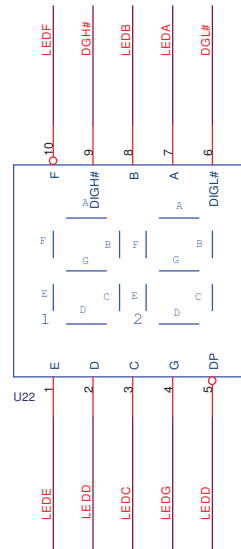
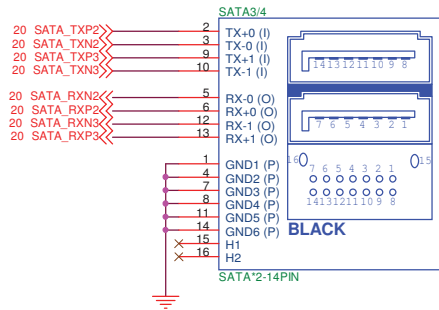
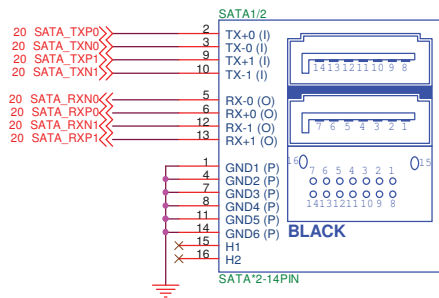




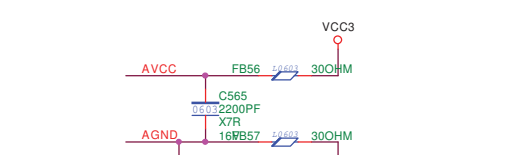
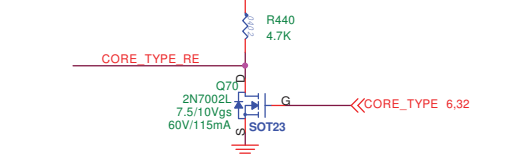
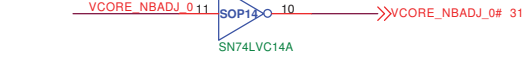
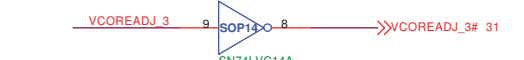
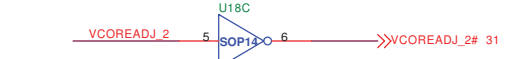
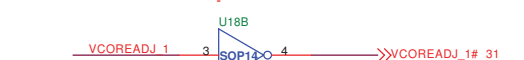
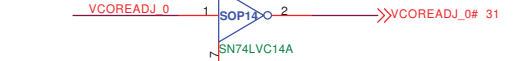
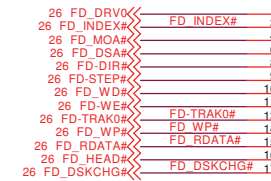
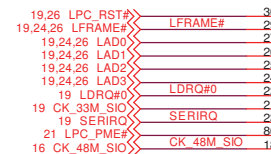
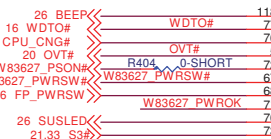
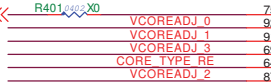
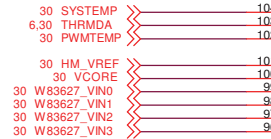
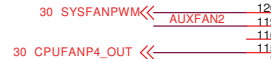
<http://laptop-motherboard-schematic.blogspot.com/>

19 AD[0..31] <<

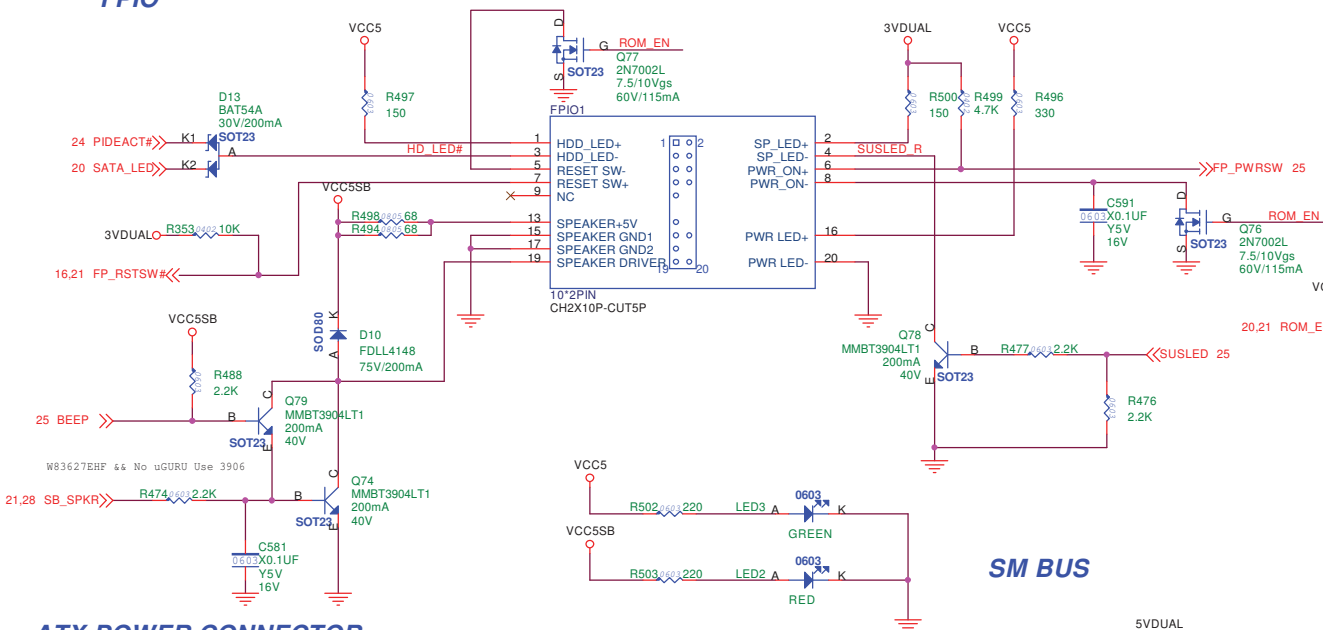
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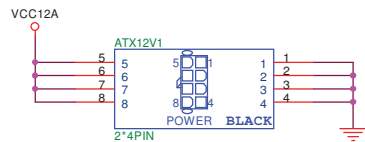
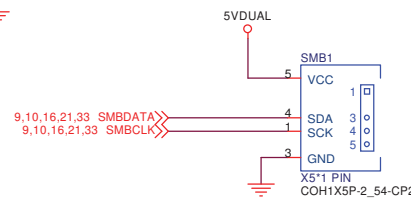
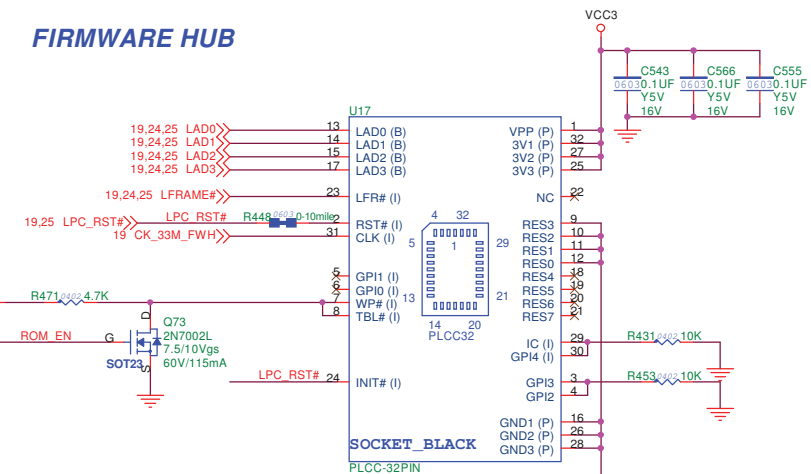
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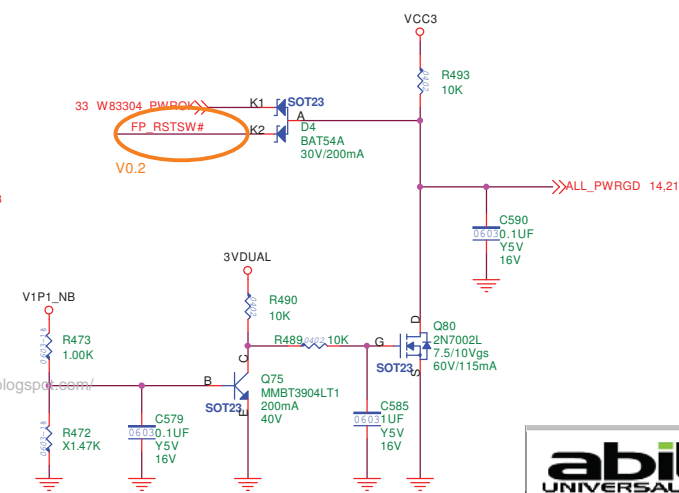
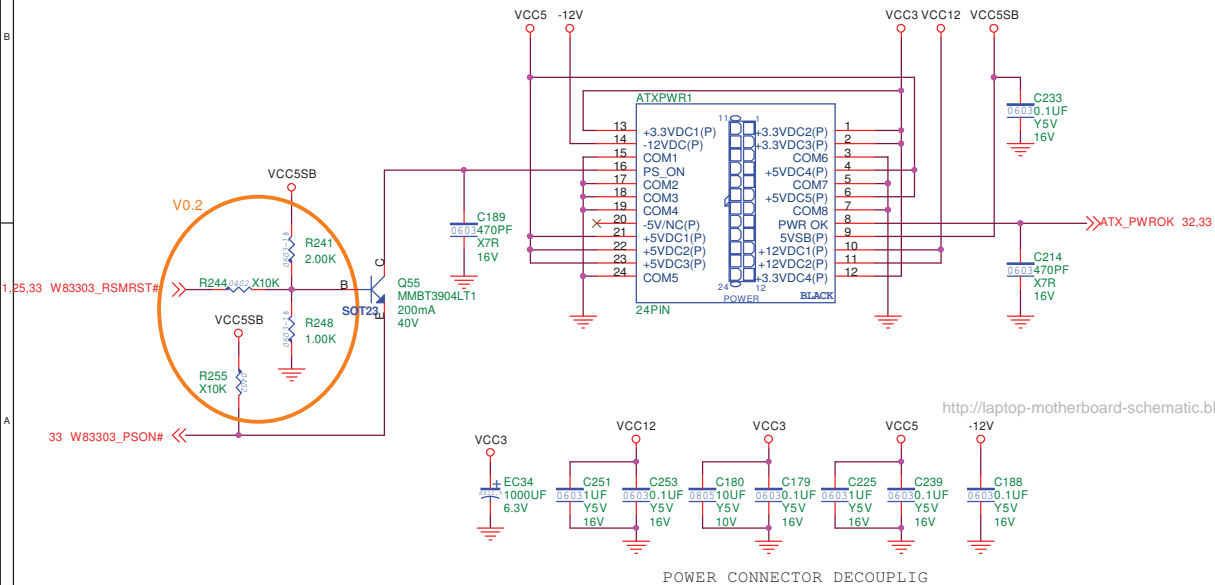
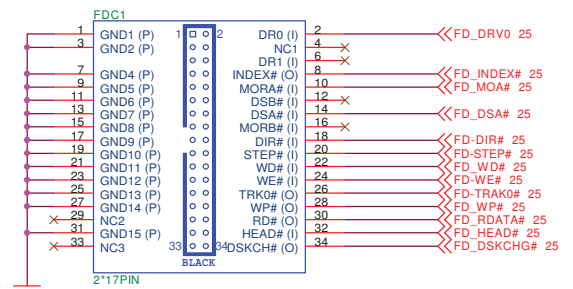
FPIO

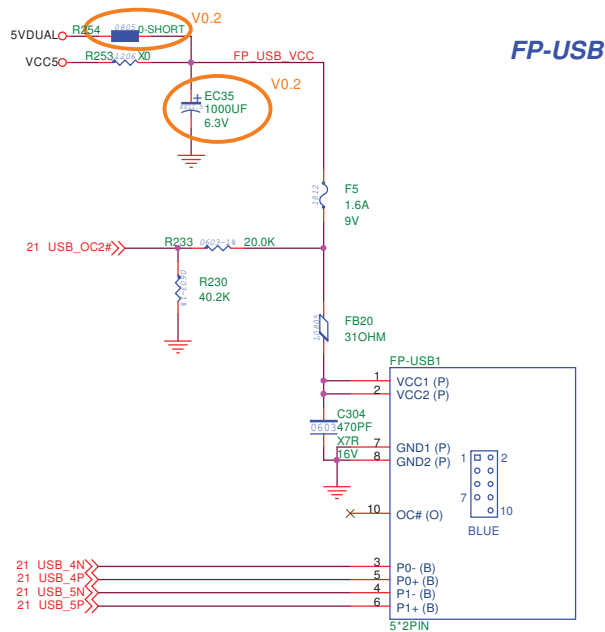


ATX POWER CONNECTOR

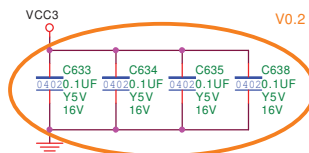
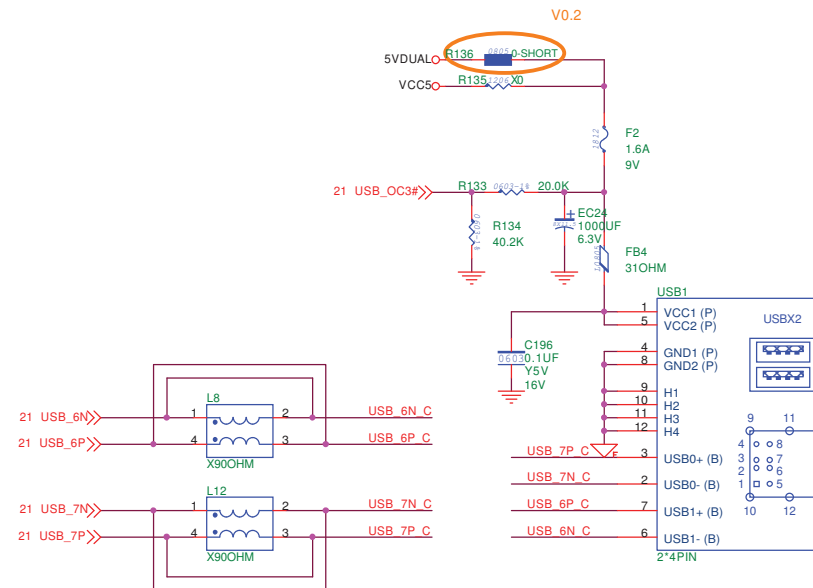
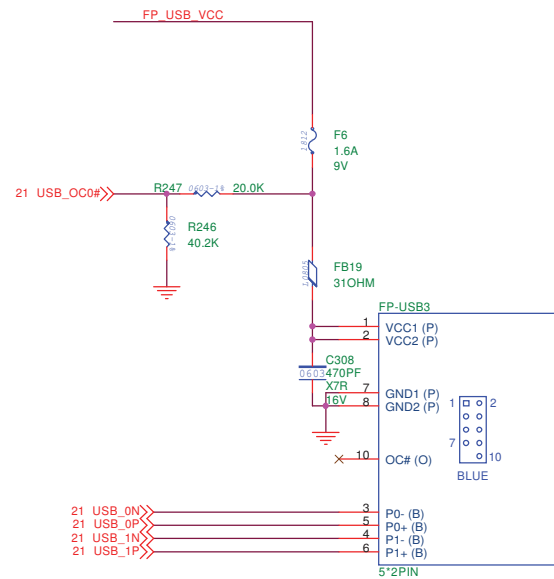
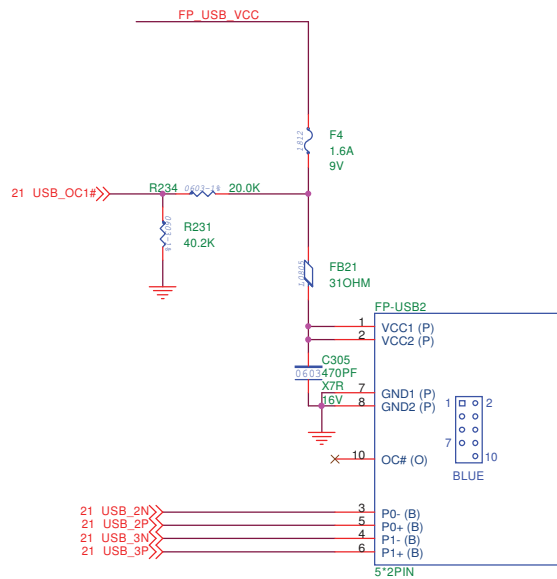
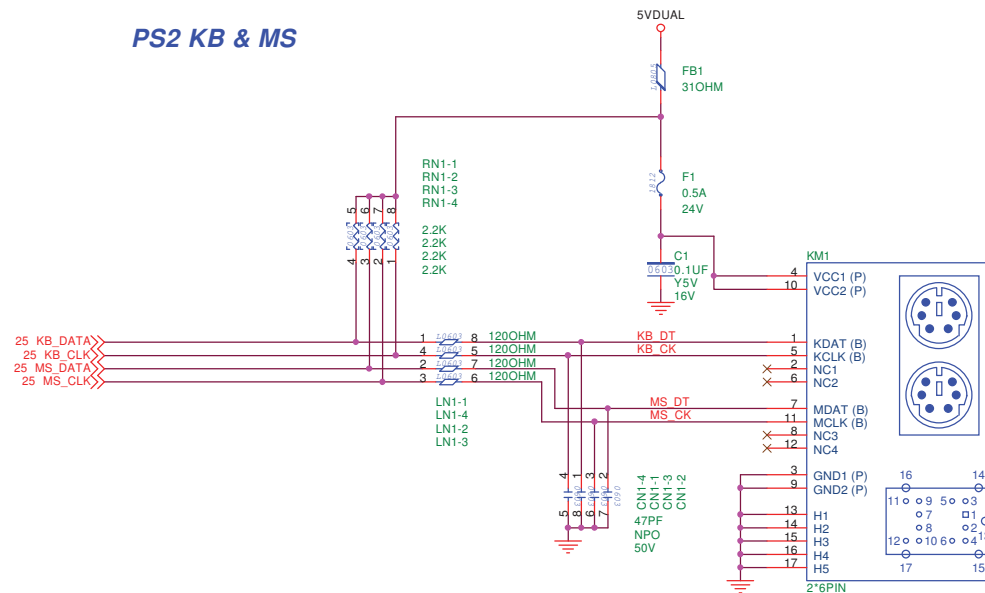
**SM BUS****FIRMWARE HUB**

FDC

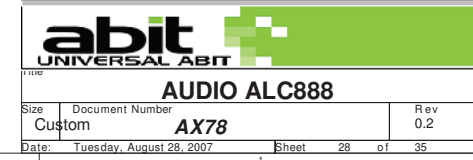
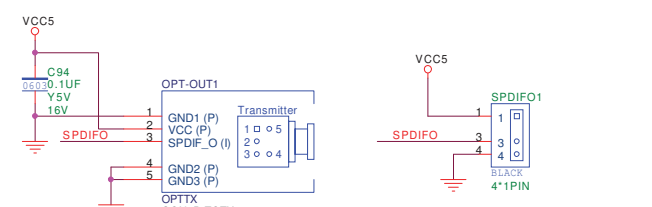
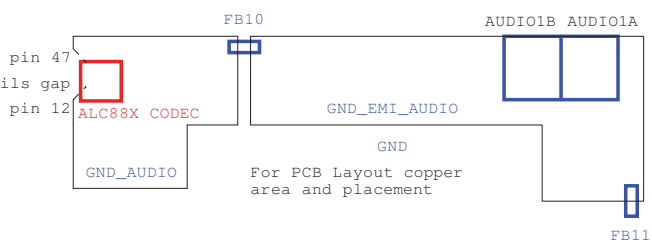




PS2 KB & MS

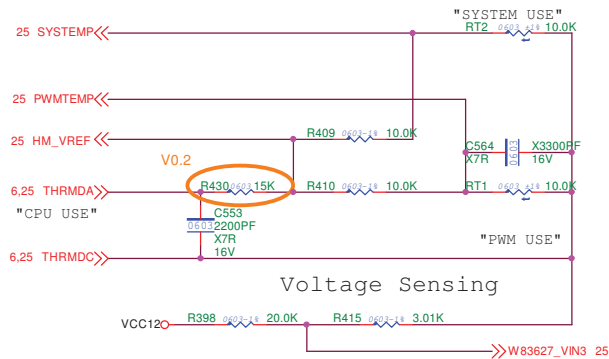


<http://laptop-motherboard-schematic.blogspot.com/>

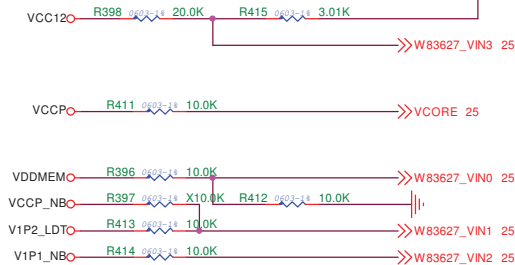


HW MONITOR

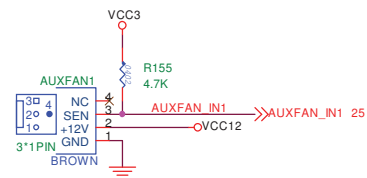
Temperature Sensing



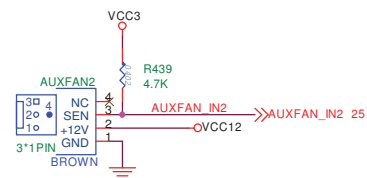
Voltage Sensing



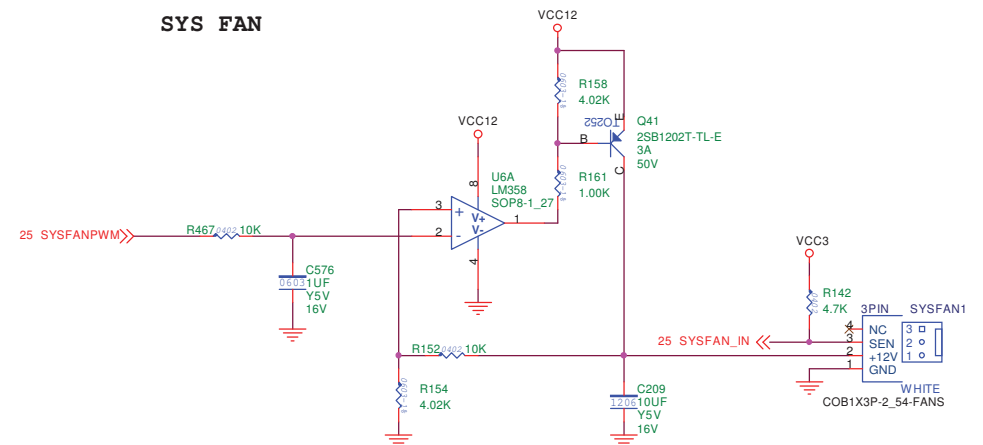
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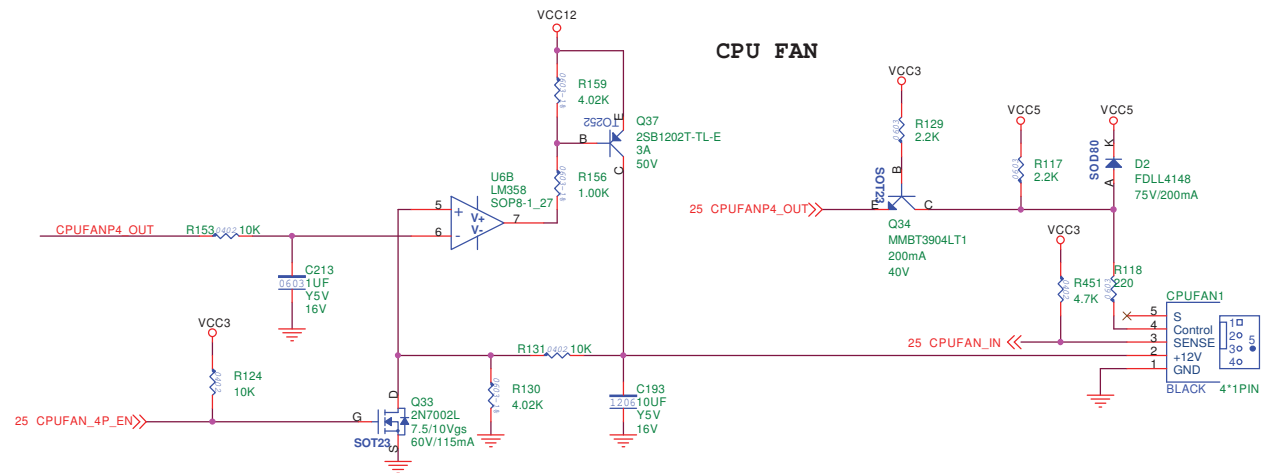
AUX FAN 2



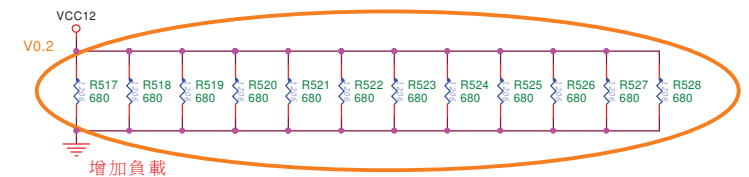
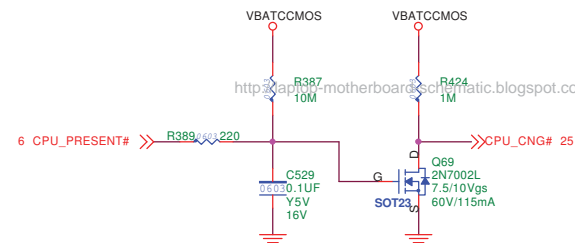
SYS FAN

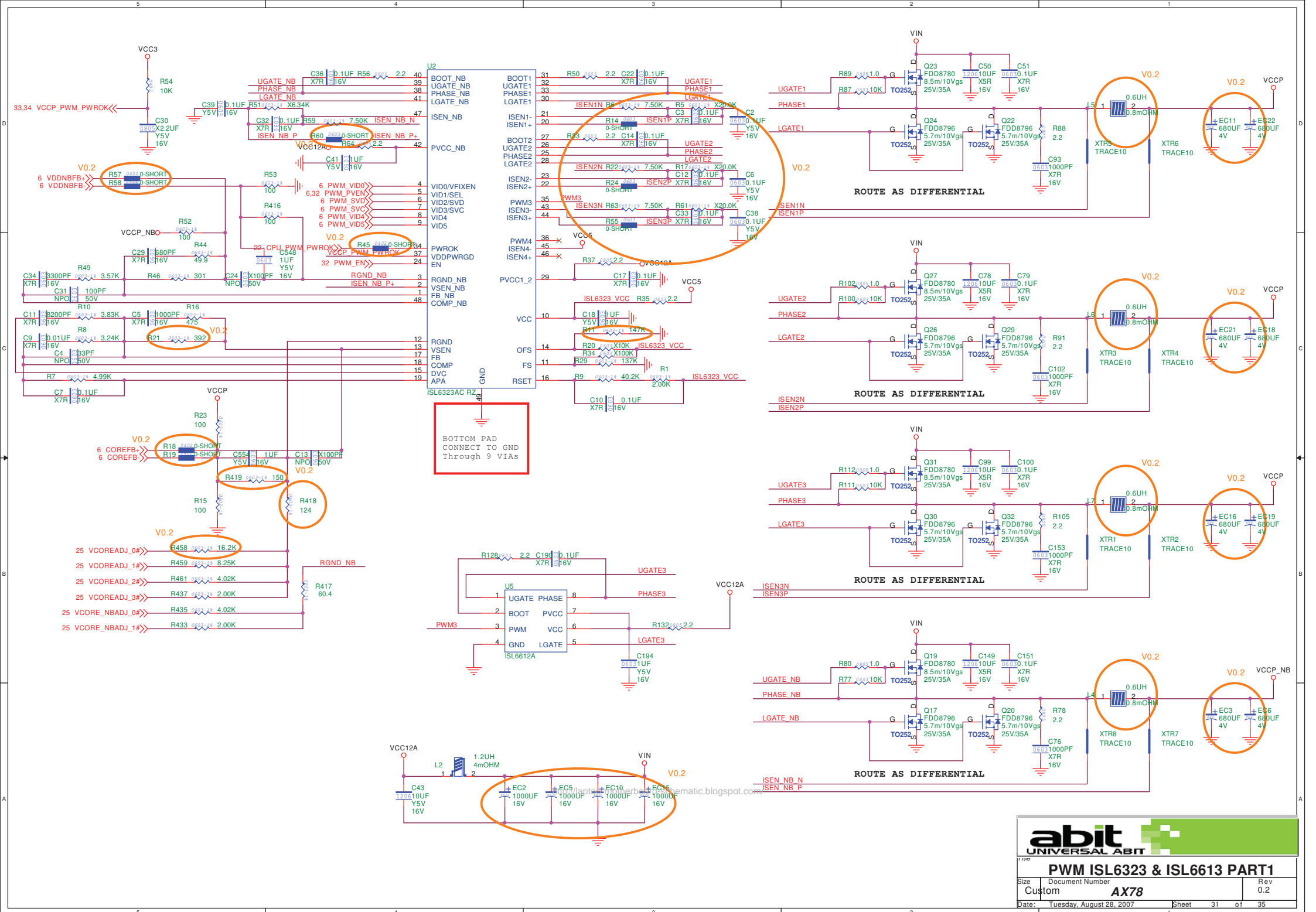


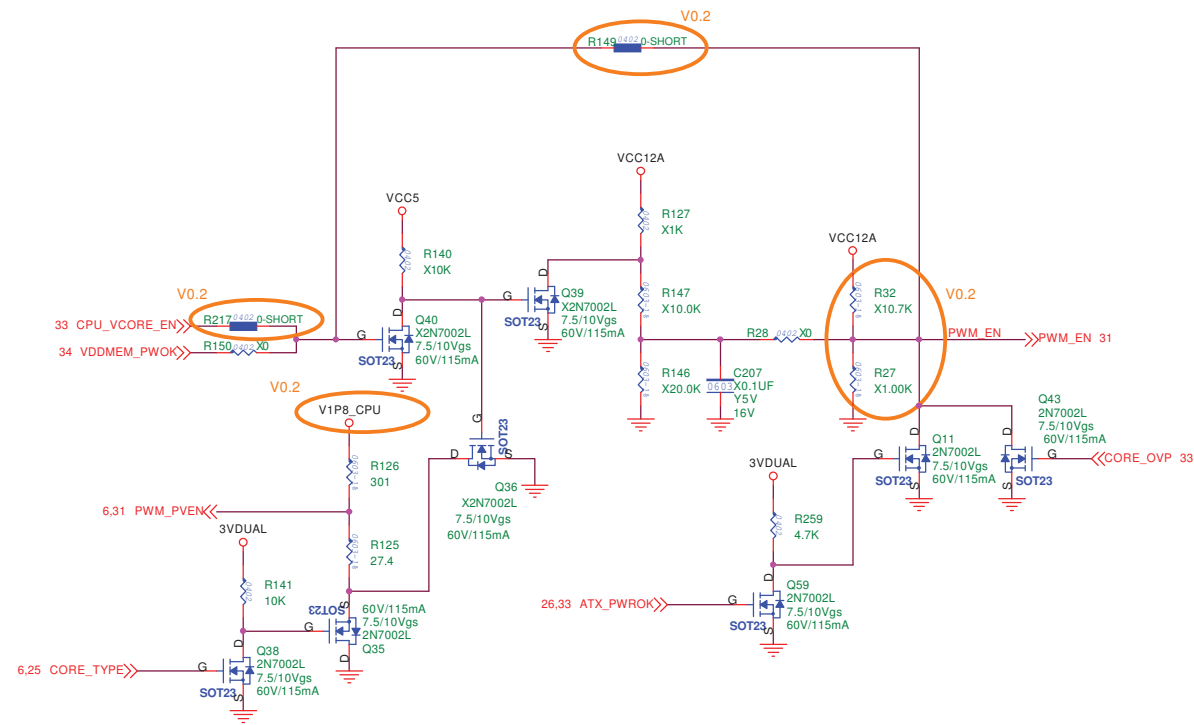
CPU FAN



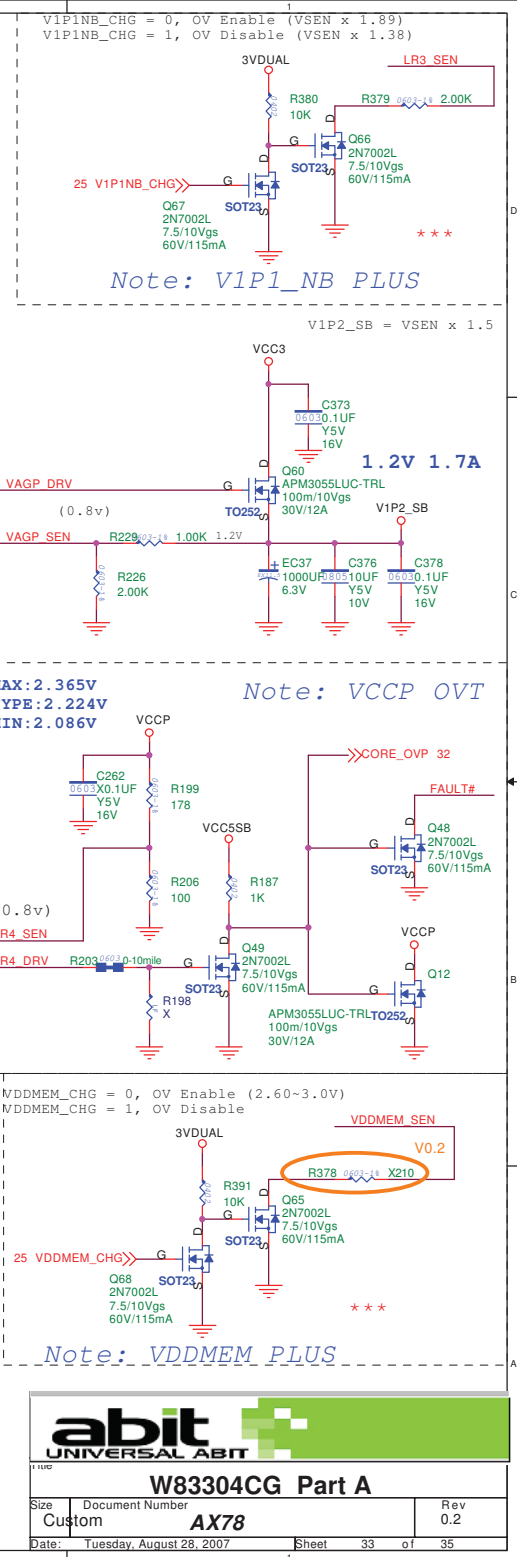
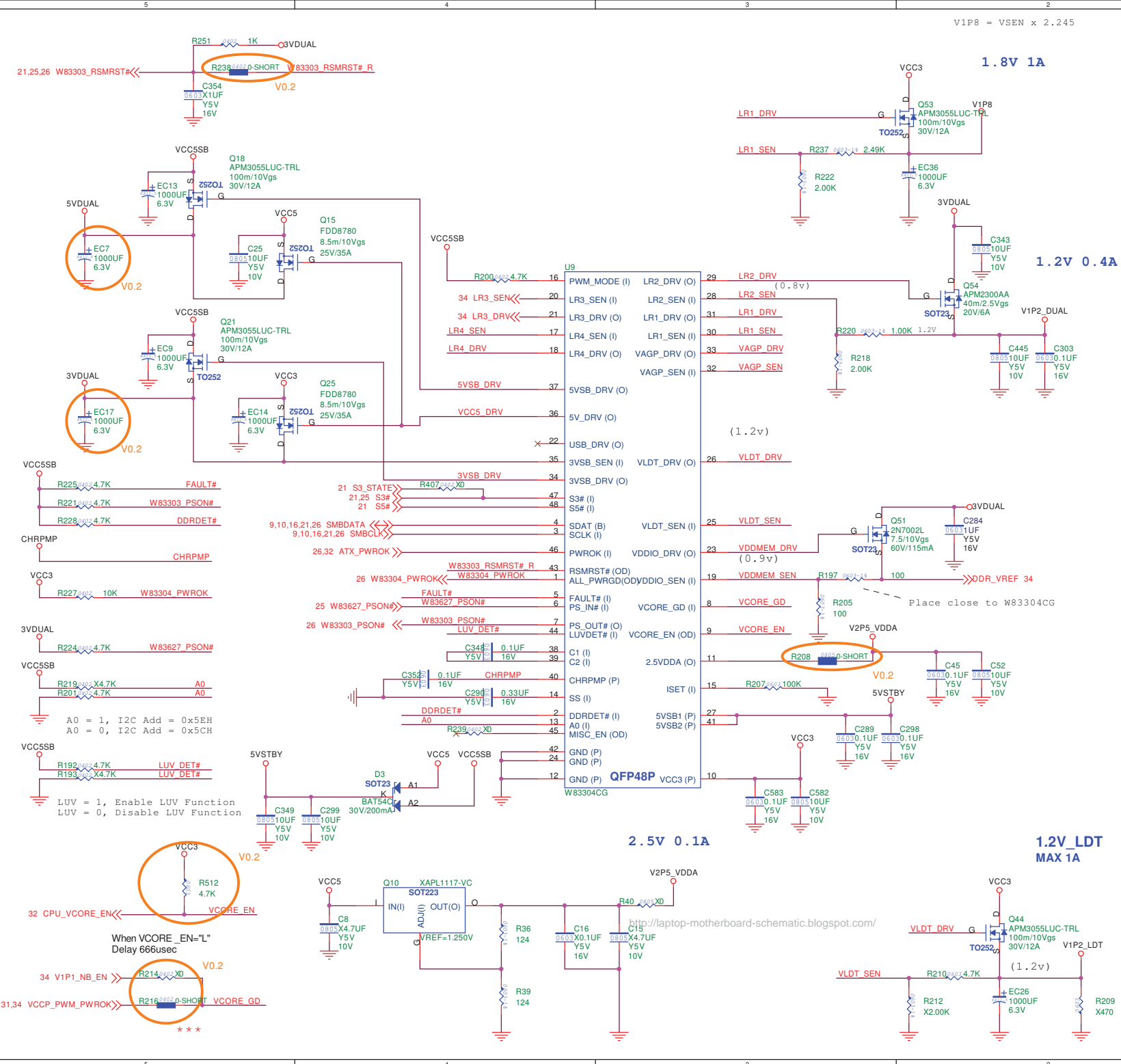
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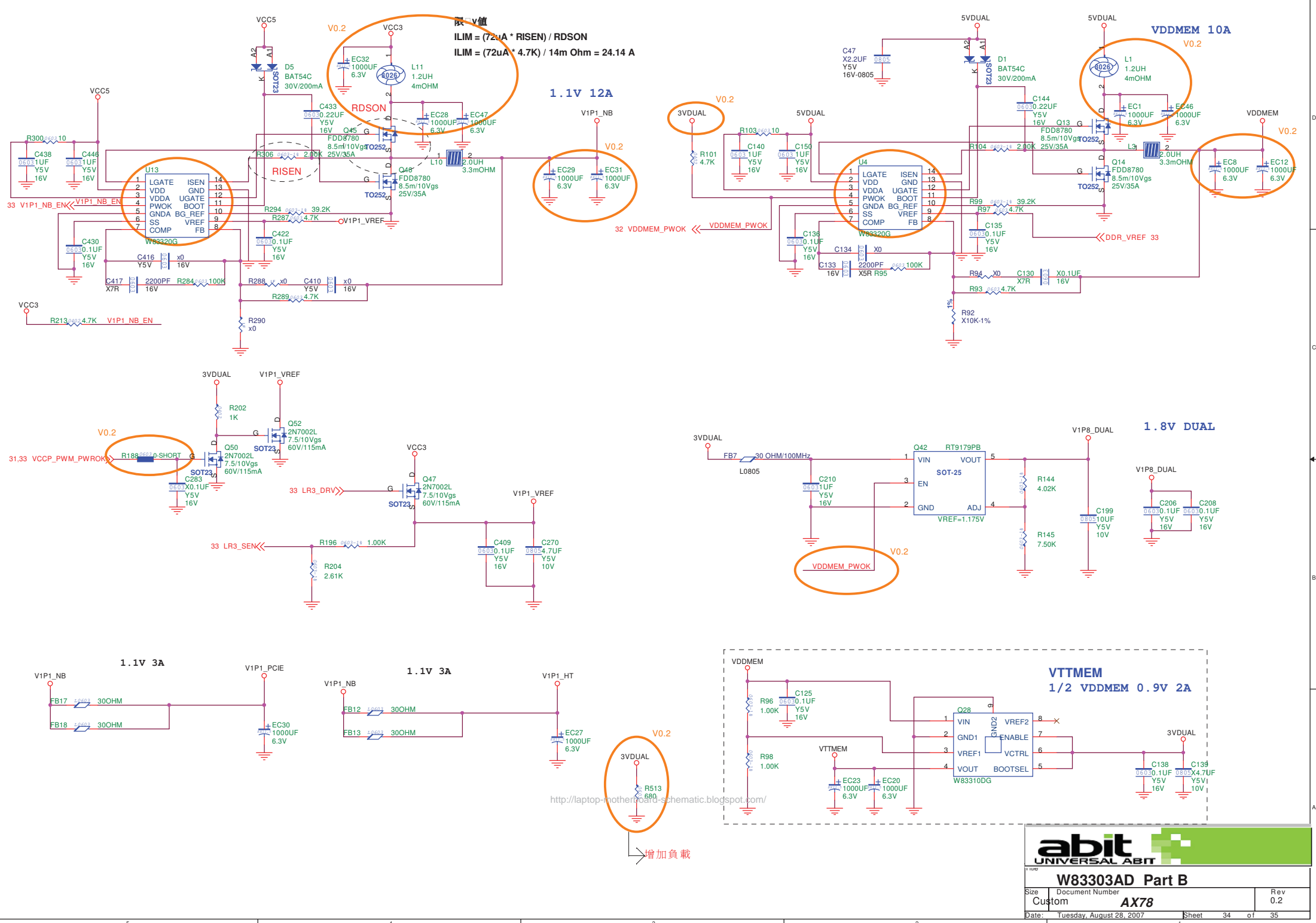







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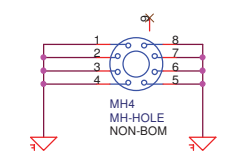
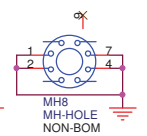
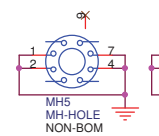
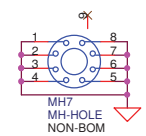
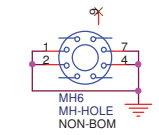
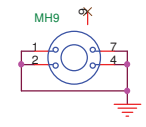
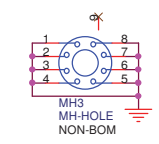
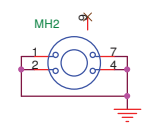
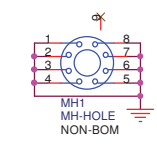
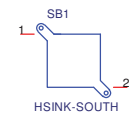
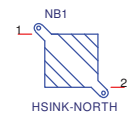
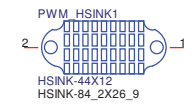
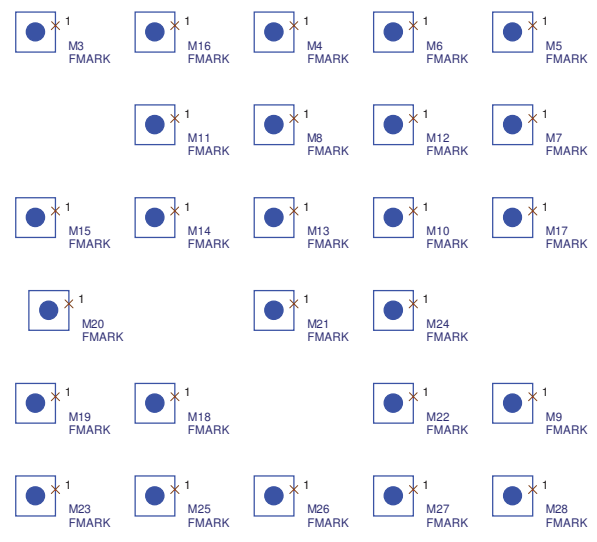
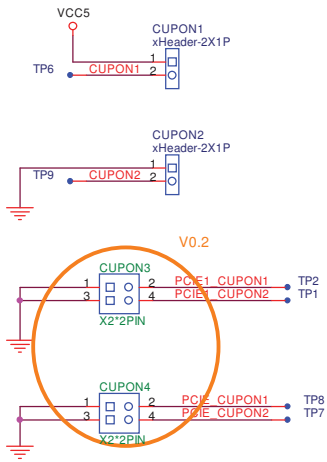
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W83303AD Part B

Size	Document Number	Rev
Custom	AX78	0.2

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